

100

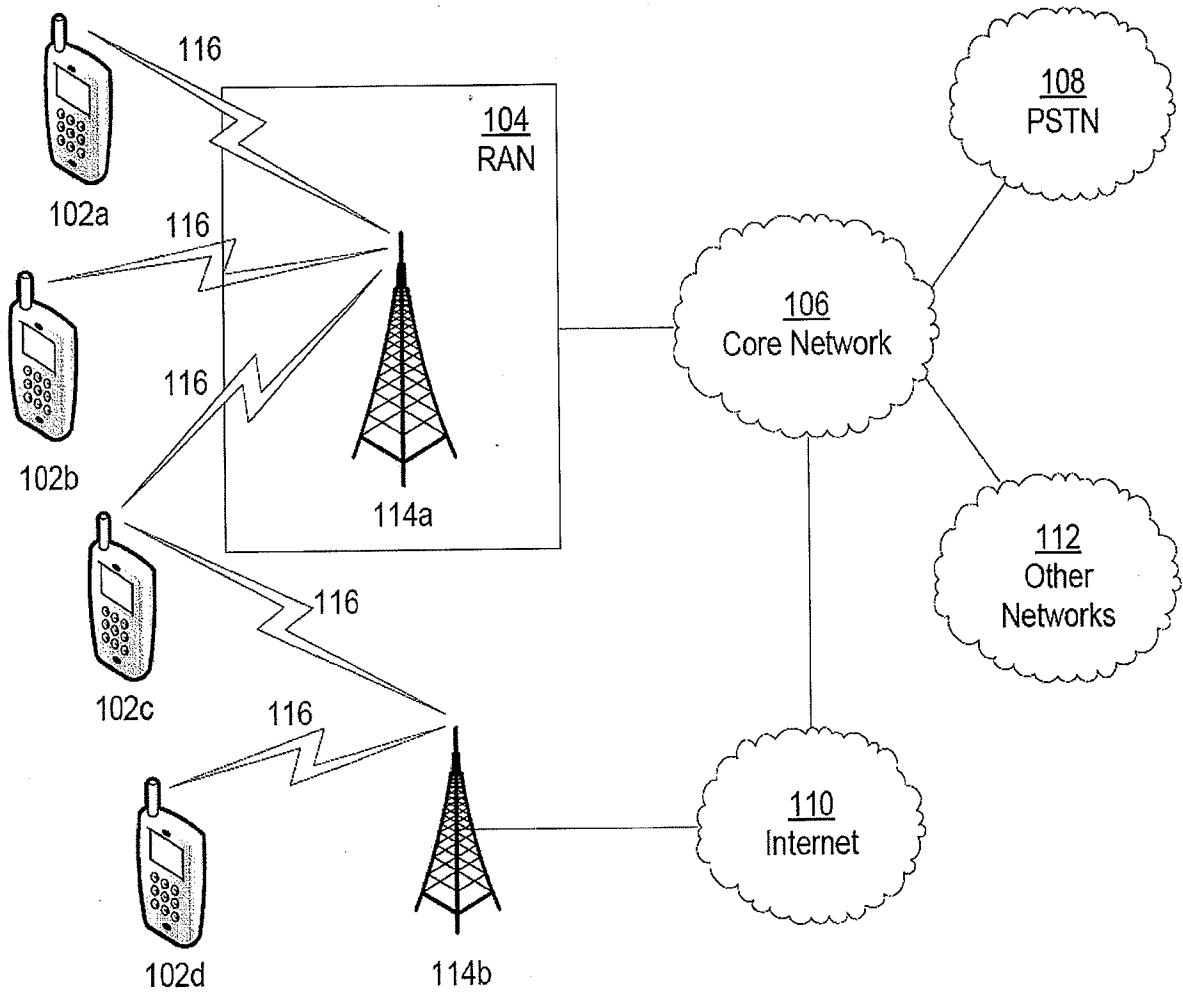


Figure 1A

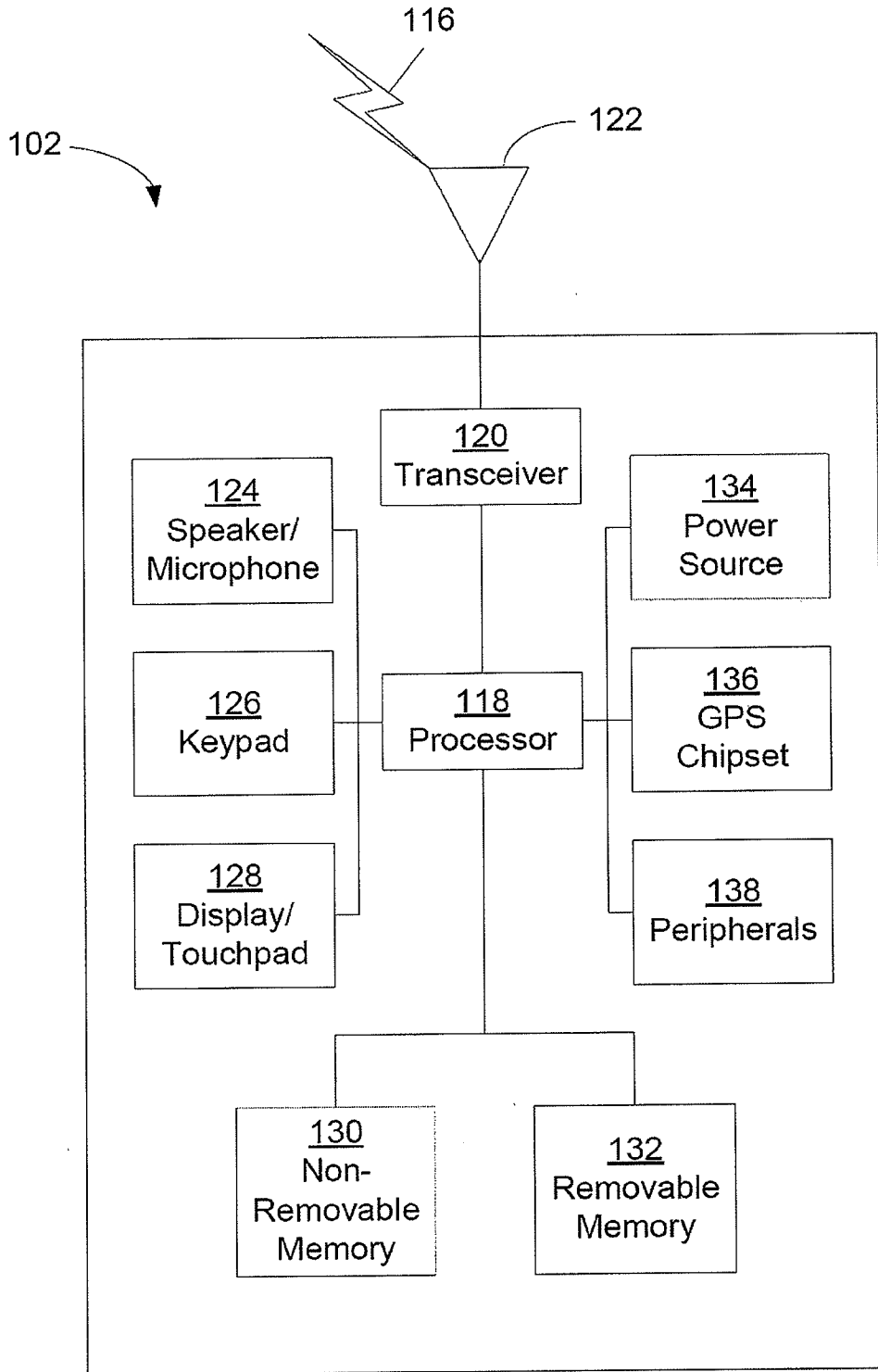


Figure 1B

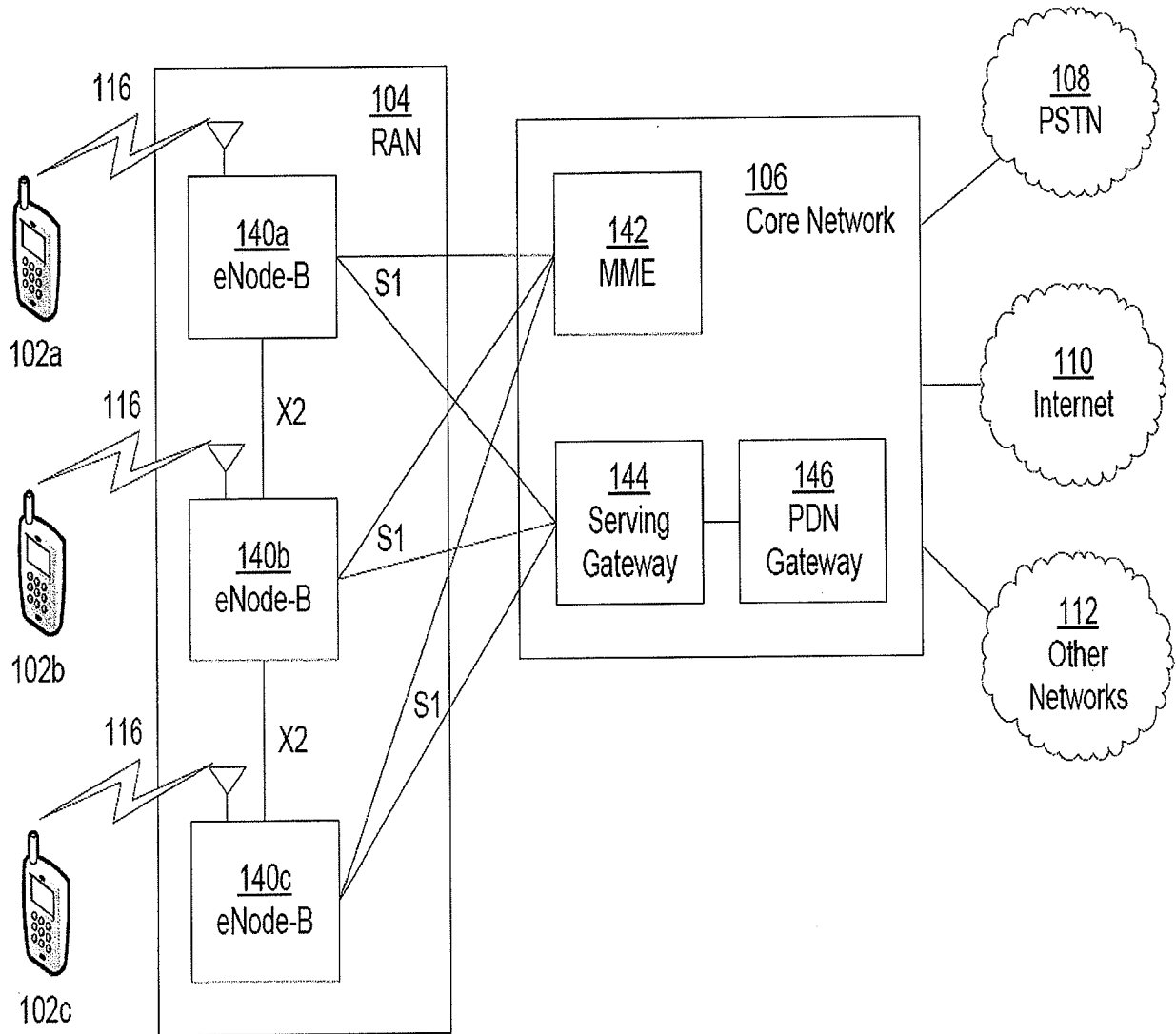


Figure 1C

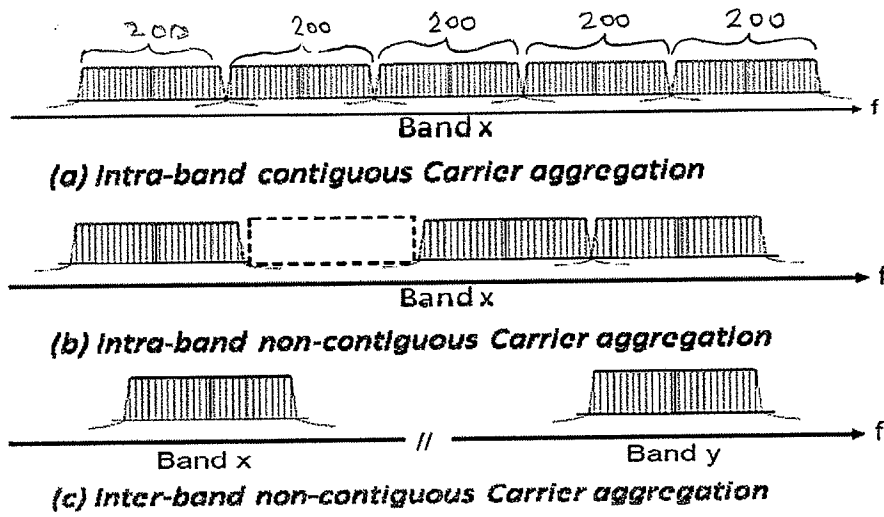


Figure 2

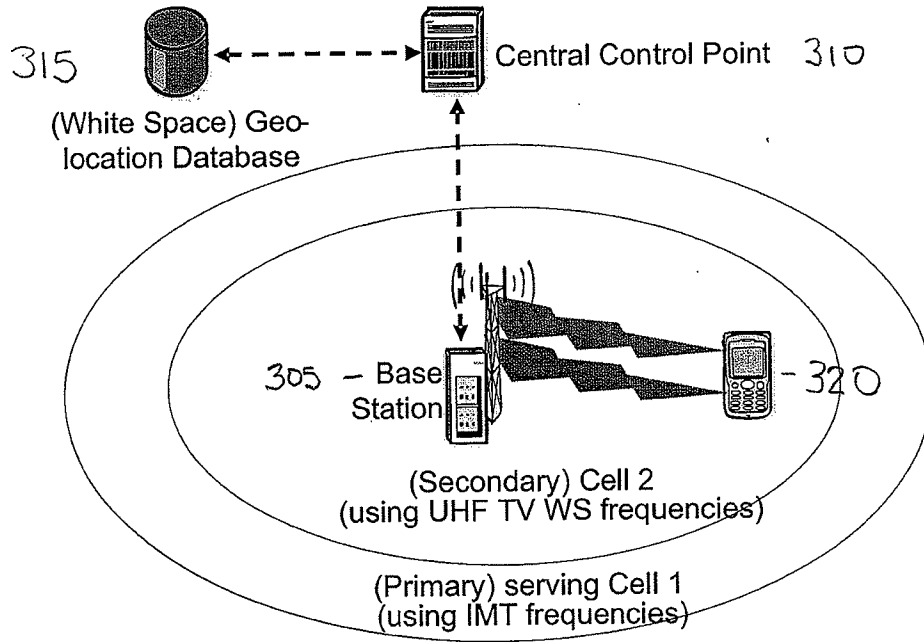


Figure 3

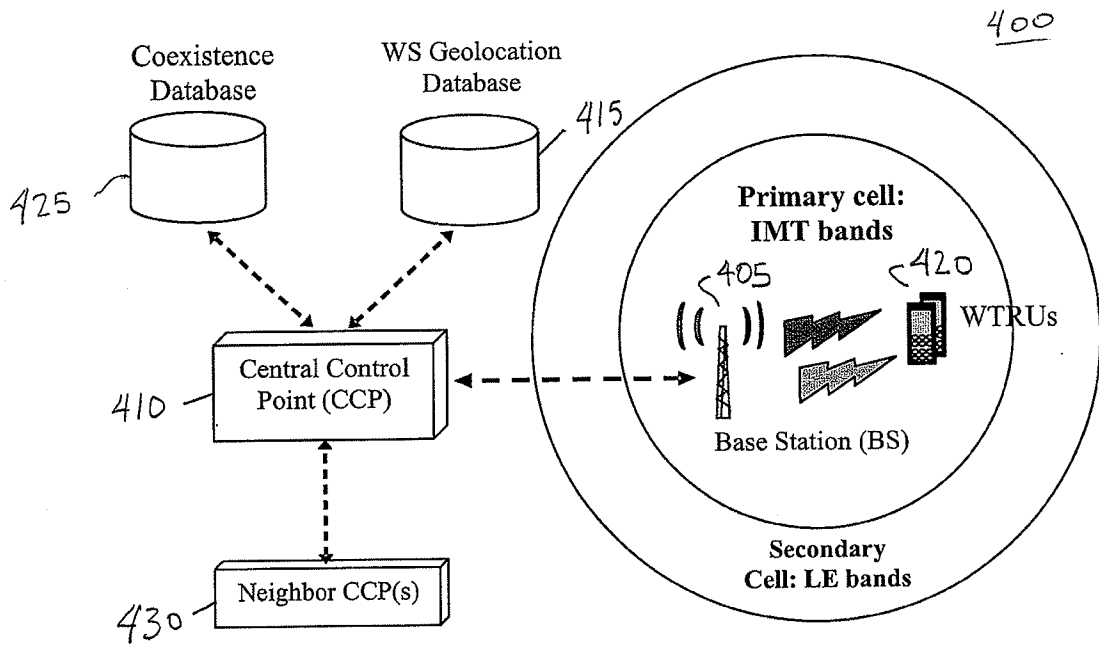


Figure 4

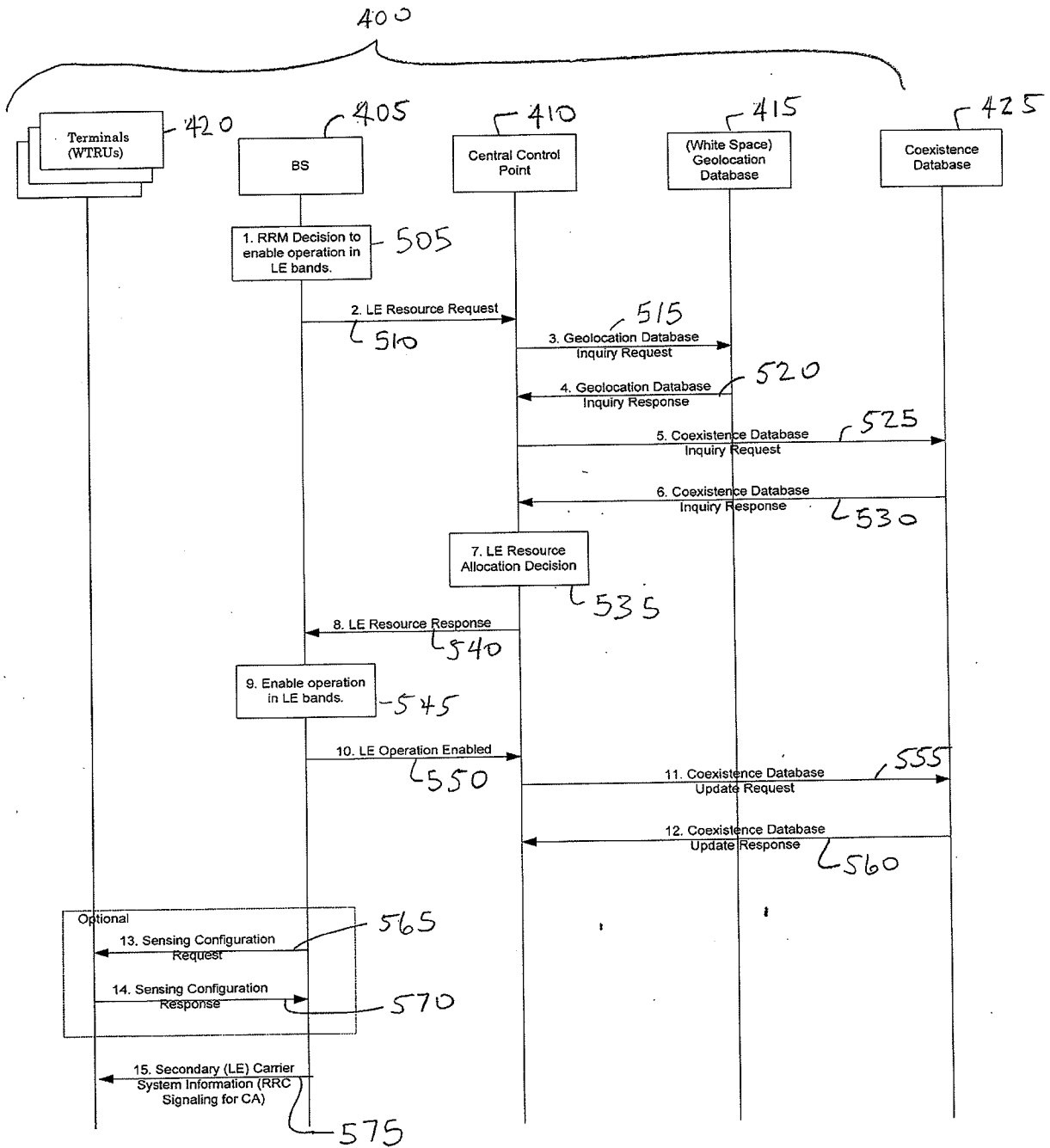


Figure 5

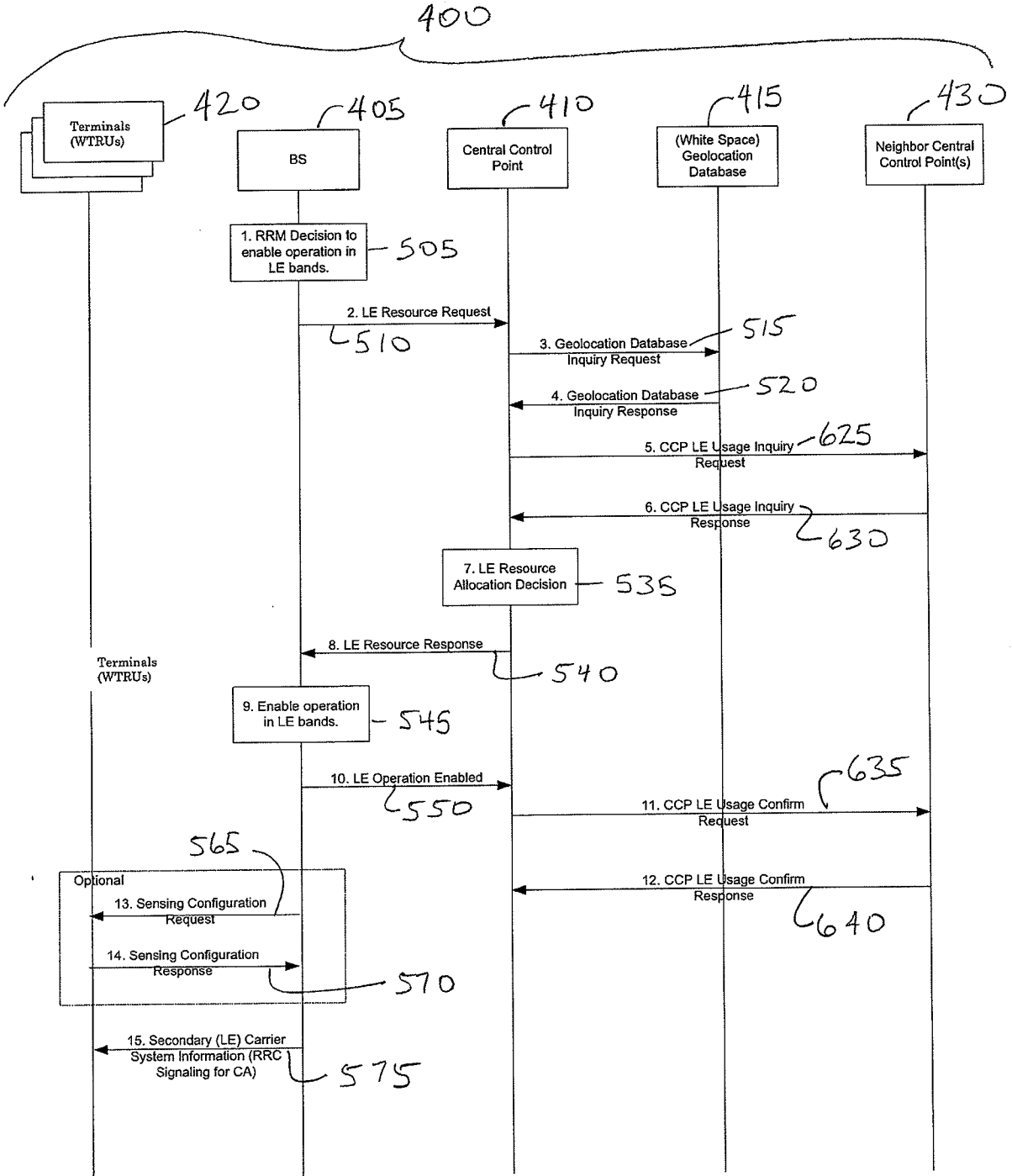


Figure 6

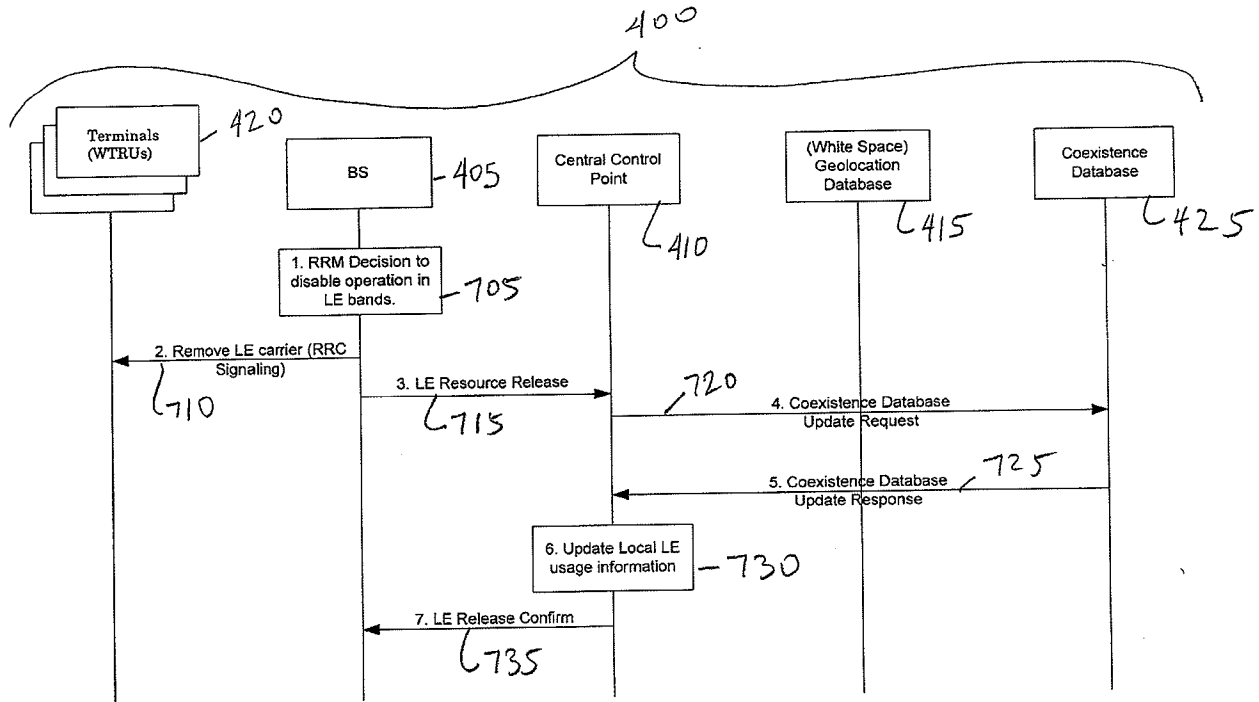


Figure 7

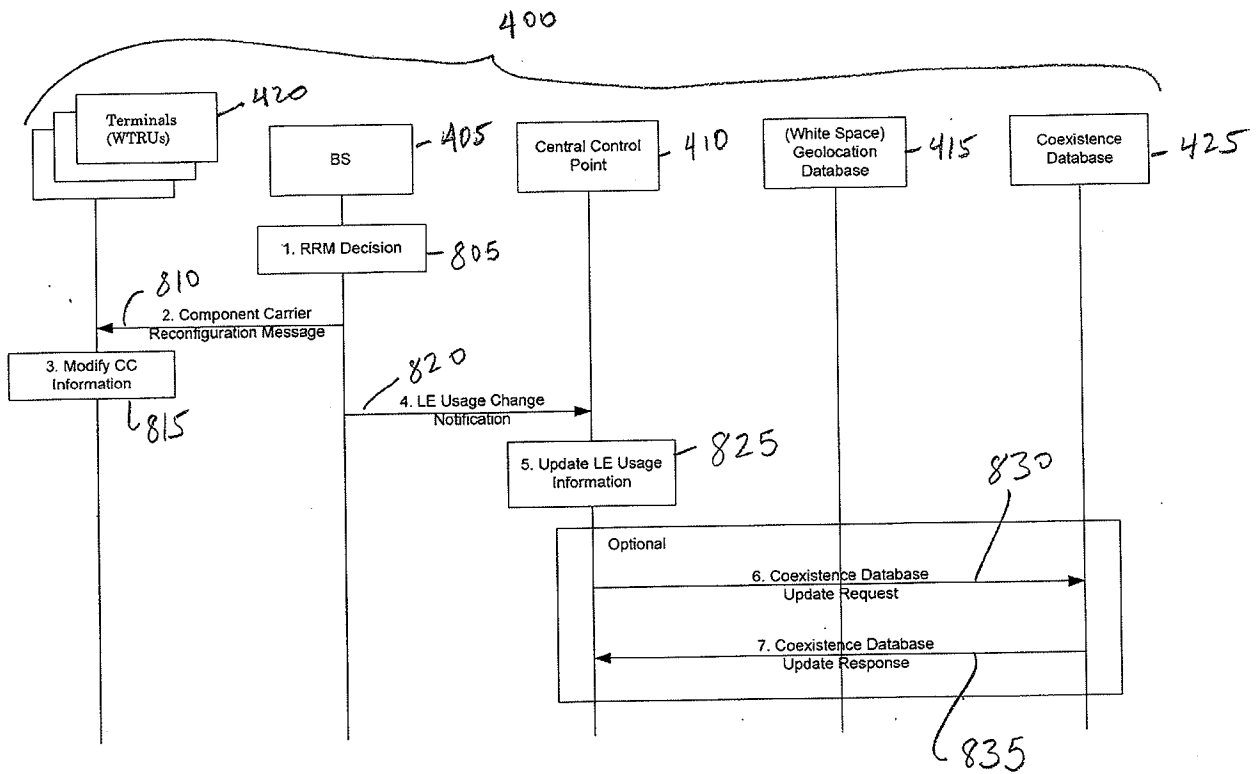


Figure 8



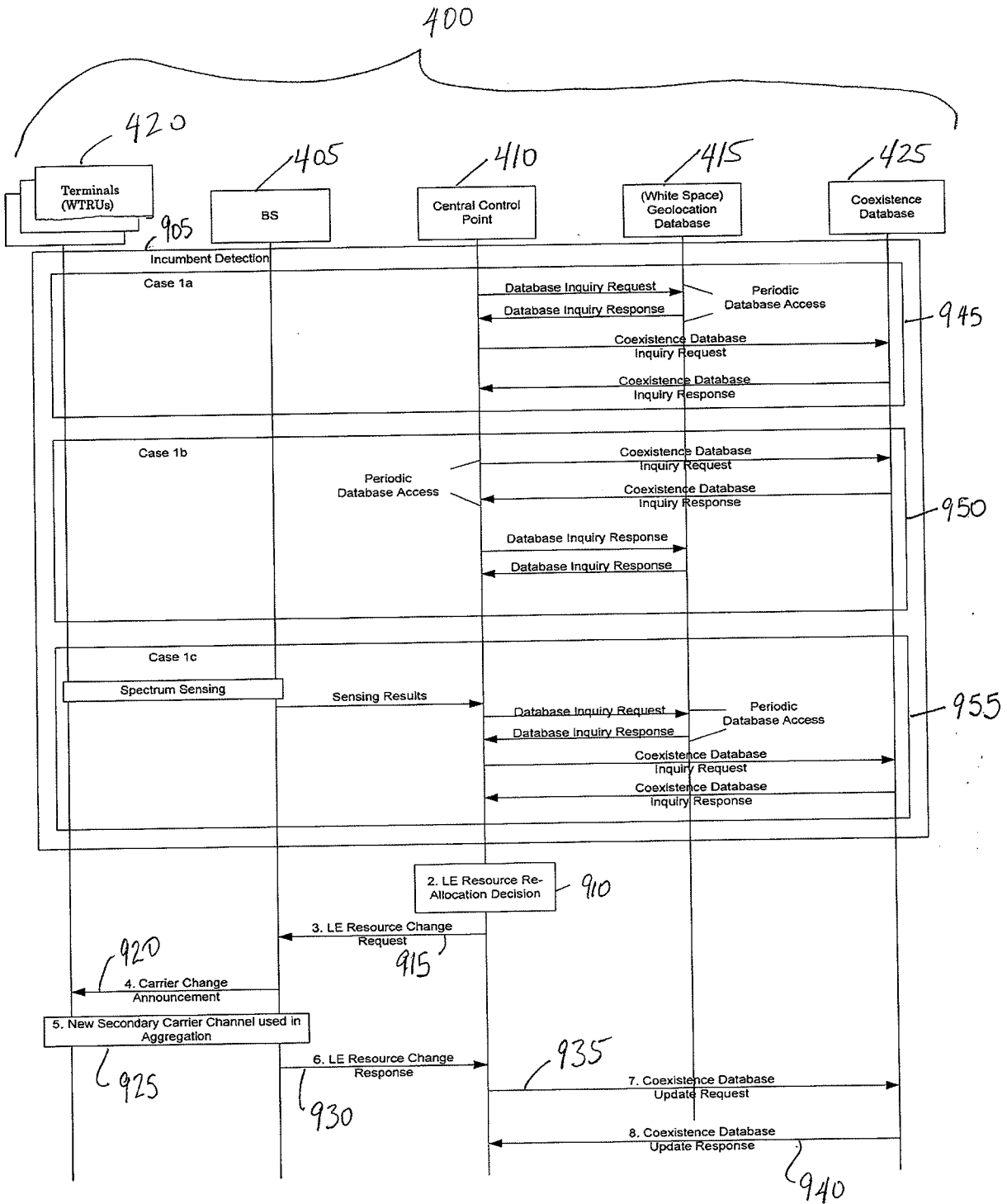


Figure 9

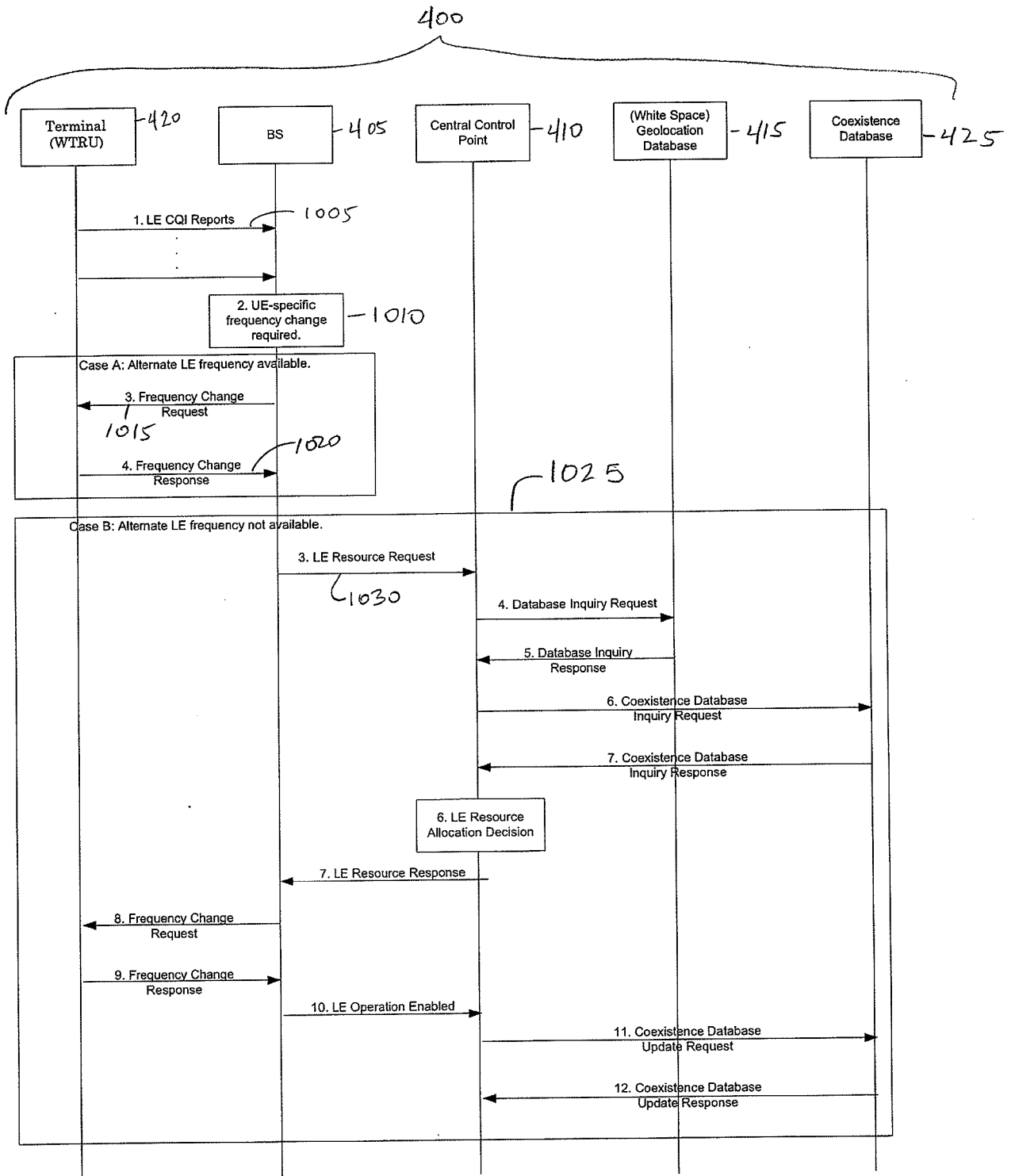


Figure 10

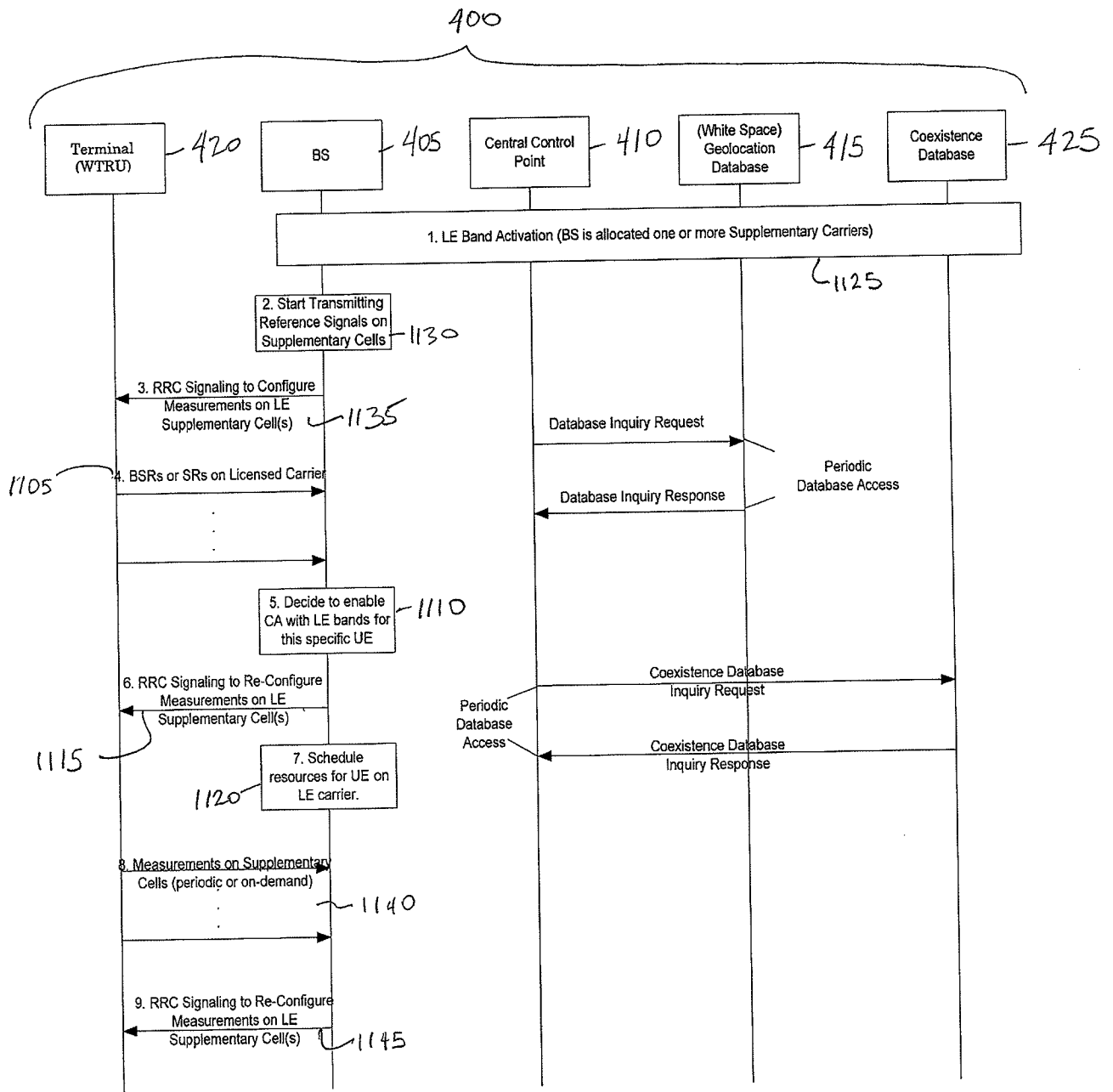


Figure 11

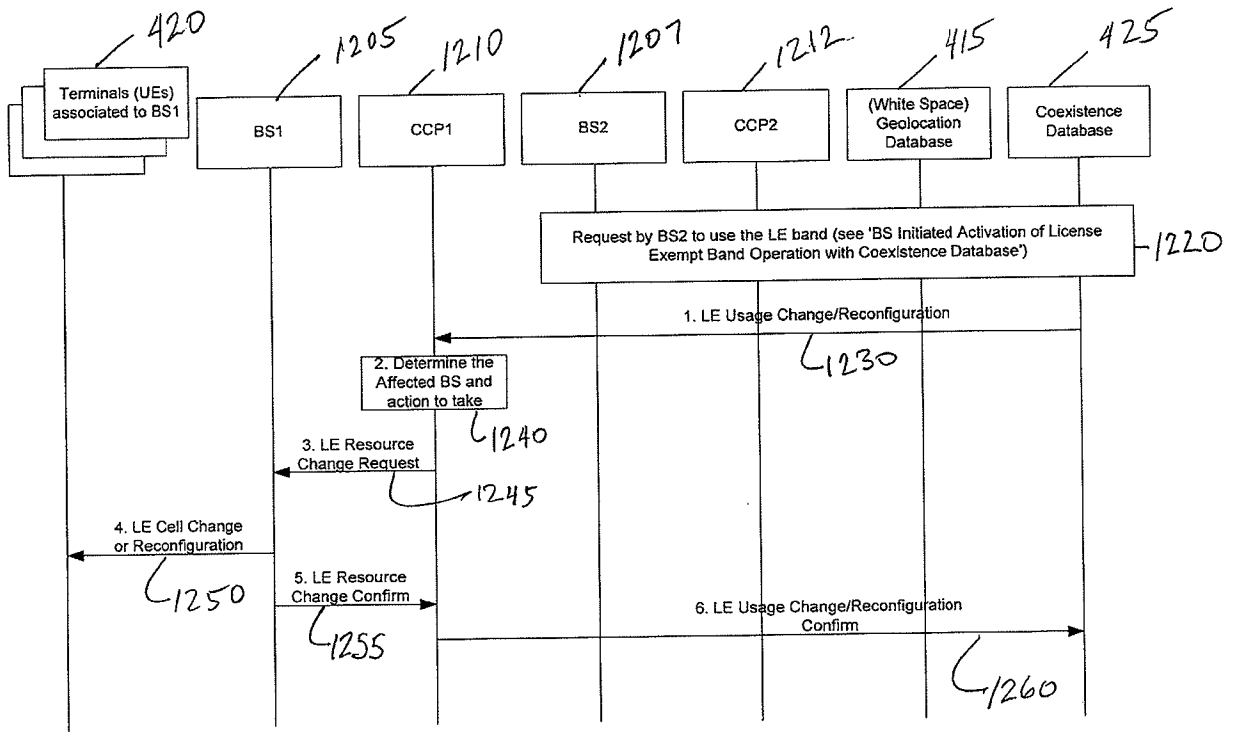


Figure 12

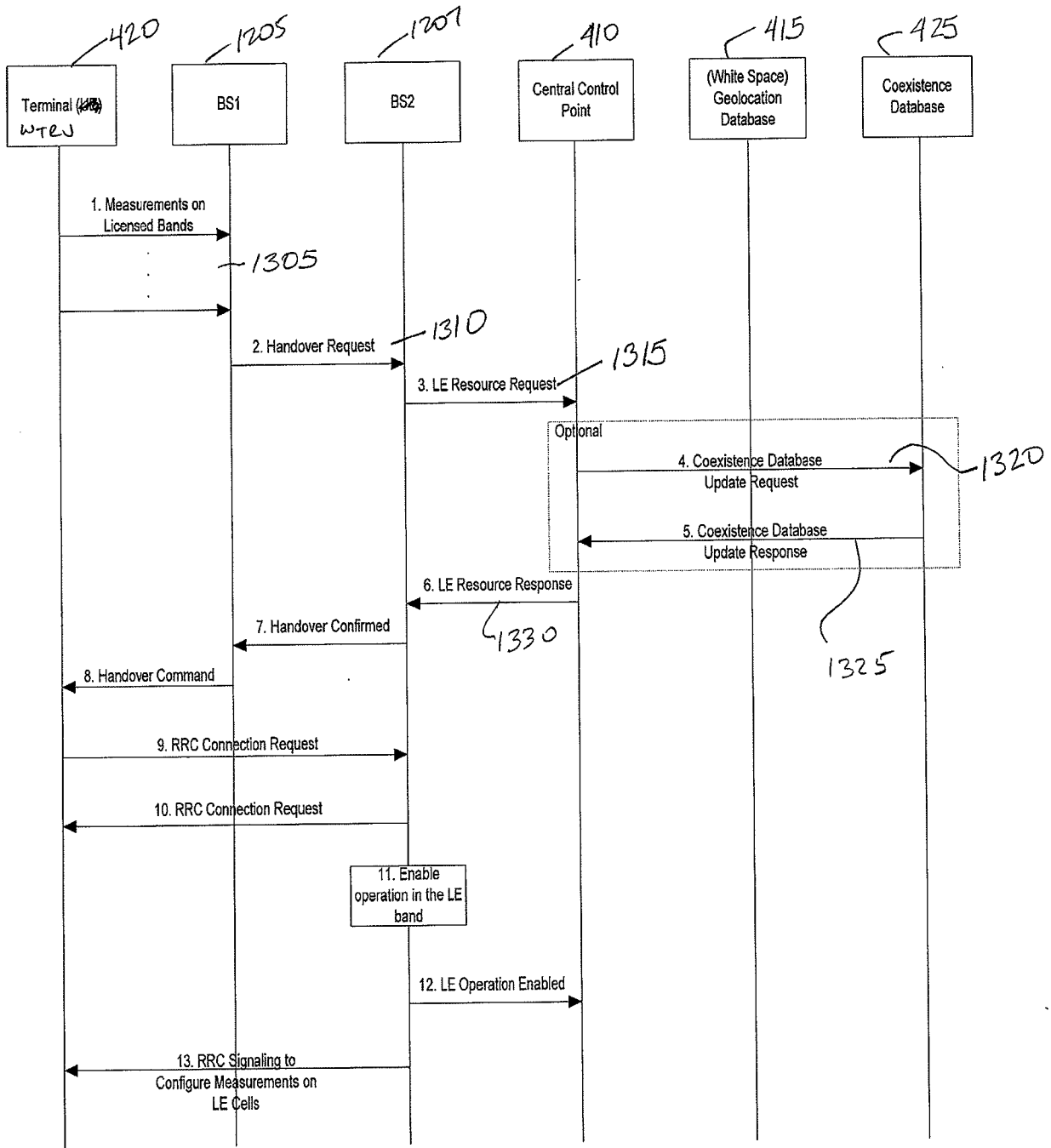


Figure 13

LE Channel IE 1	- frequency & BW - Coexistence Rules - Sensing Rules	PCI 1
		⋮
		PCI m1
LE Channel IE 2	- frequency & BW - Coexistence Rules - Sensing Rules	PCI 1
		⋮
		PCI m2
LE Channel IE 3	- frequency & BW - Coexistence Rules - Sensing Rules	PCI 1
		⋮
		PCI m3
LE Channel IE n	- frequency & BW - Coexistence Rules - Sensing Rules	PCI 1
		⋮
		PCI mn

Figure 14

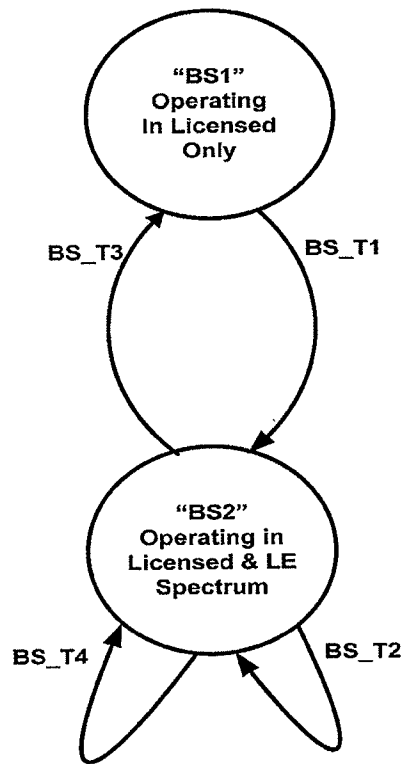


Figure 15

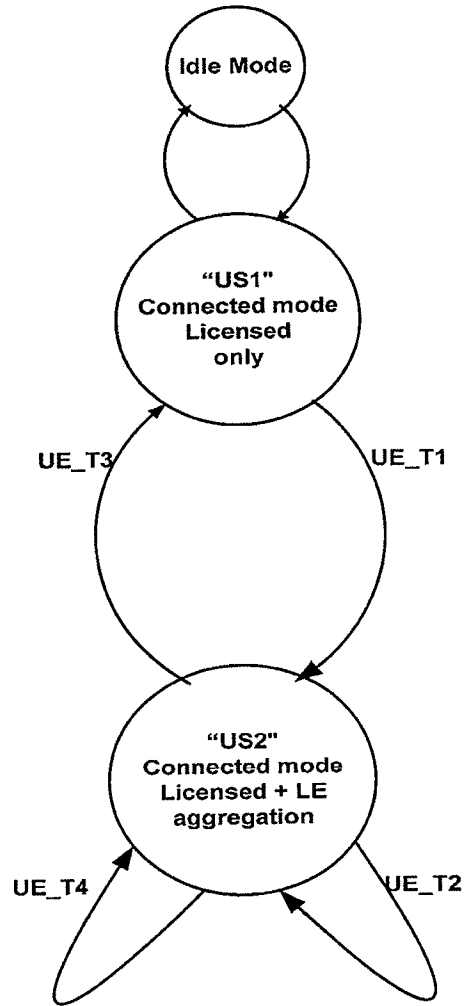


Figure 16



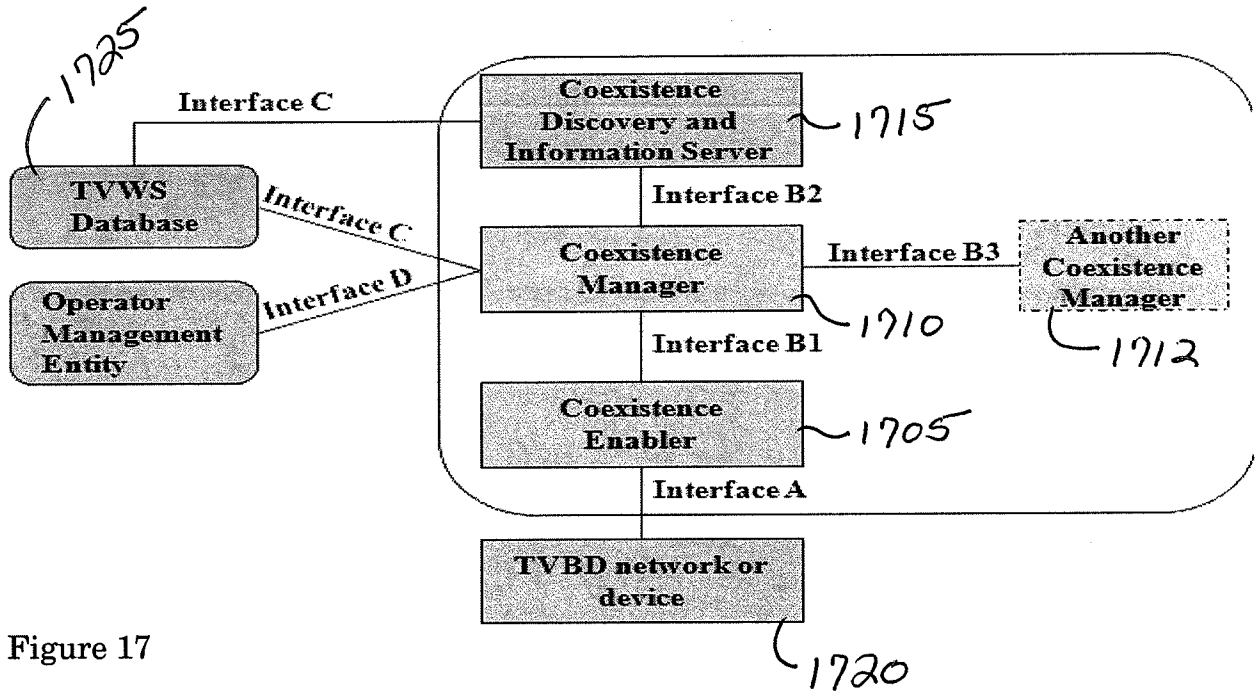


Figure 17

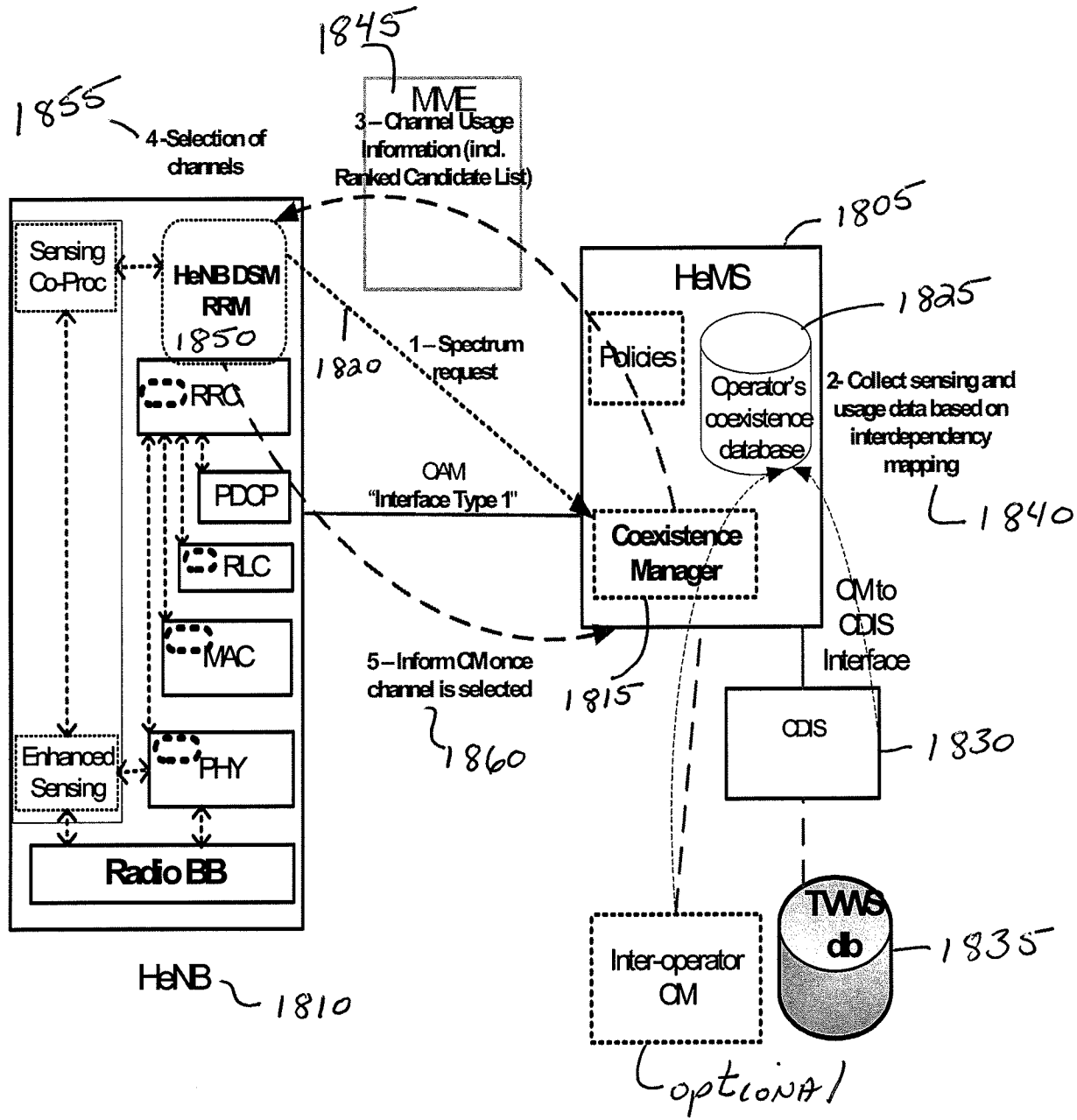


Figure 18

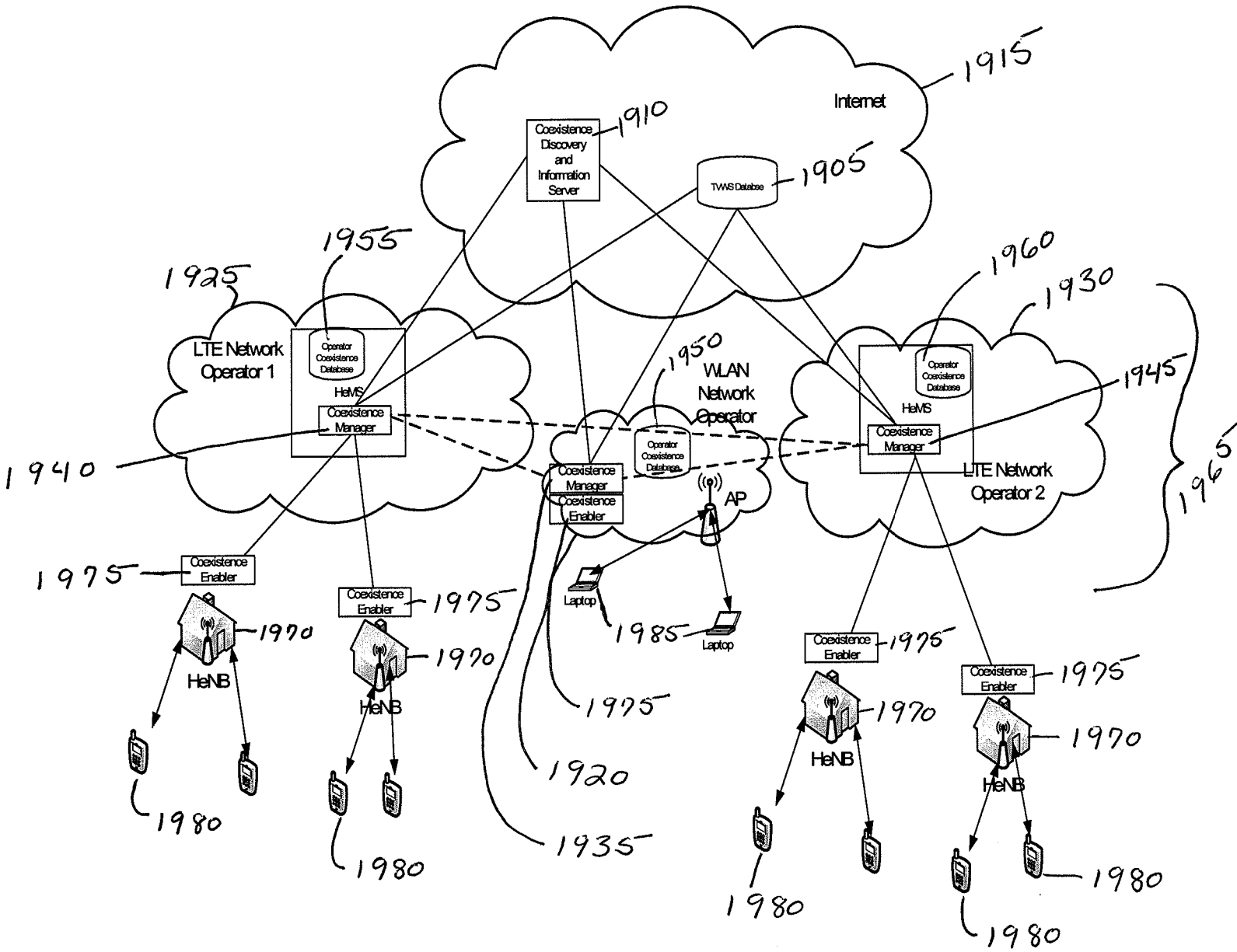


Figure 19

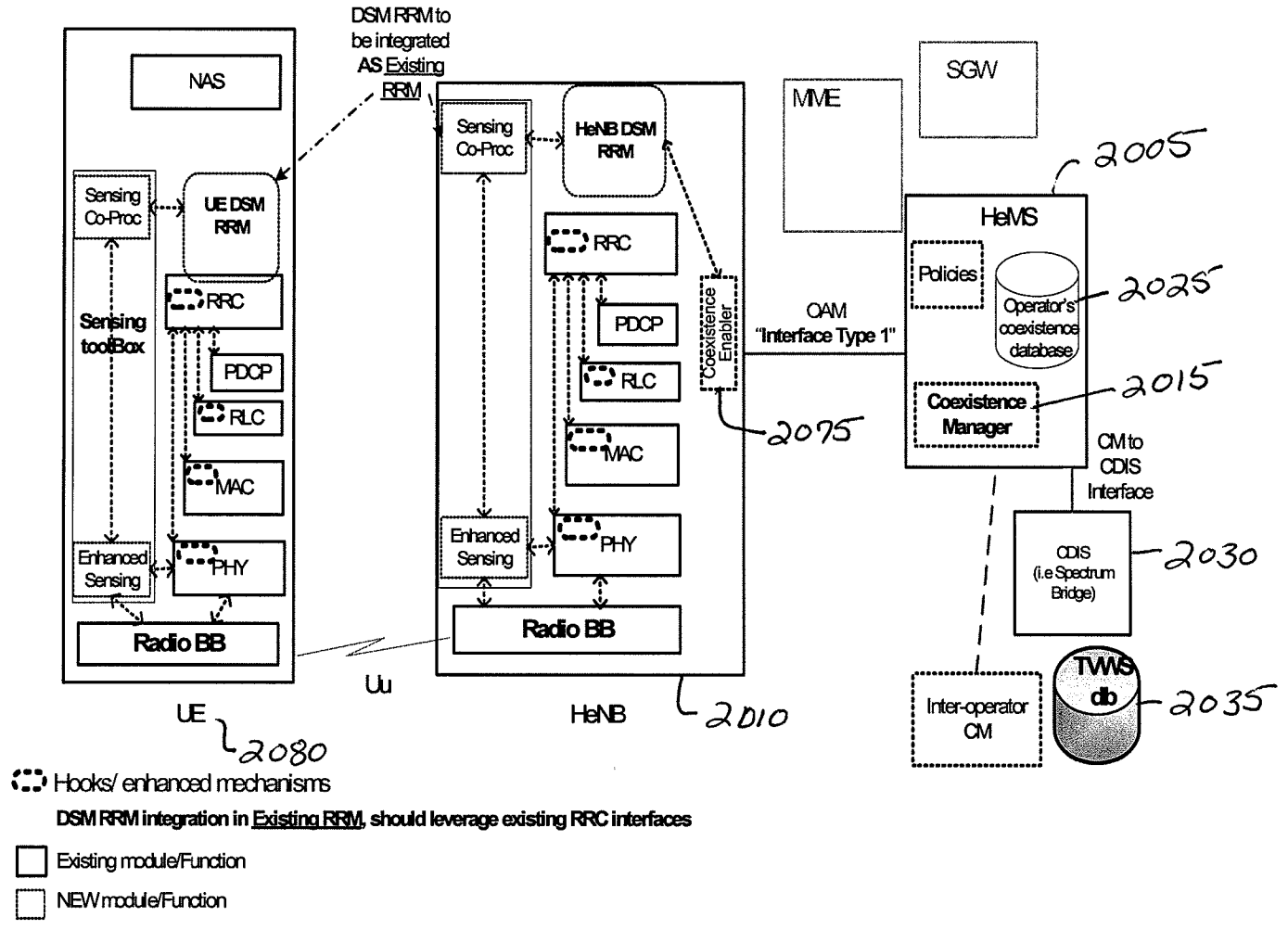


Figure 20

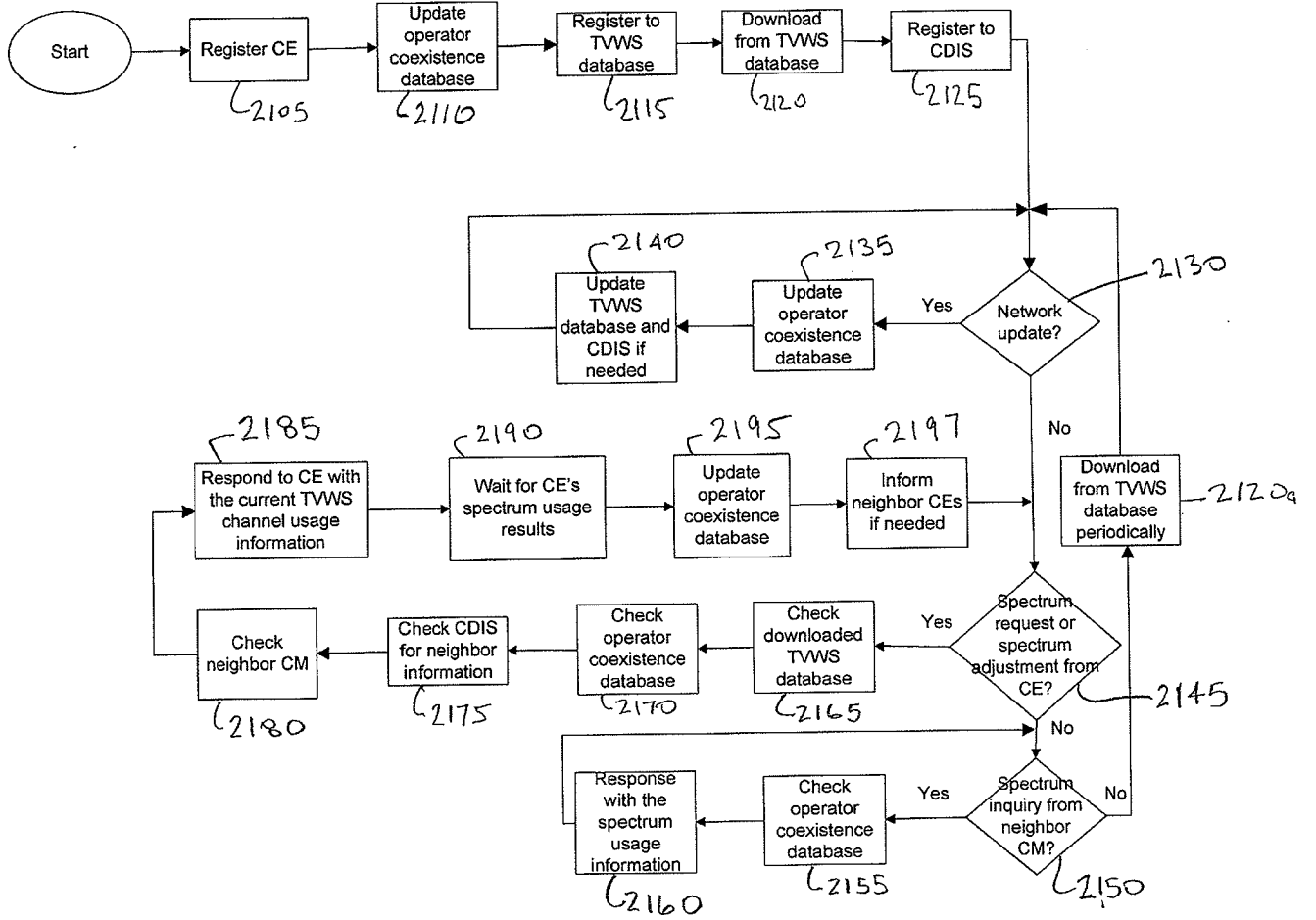


Figure 21

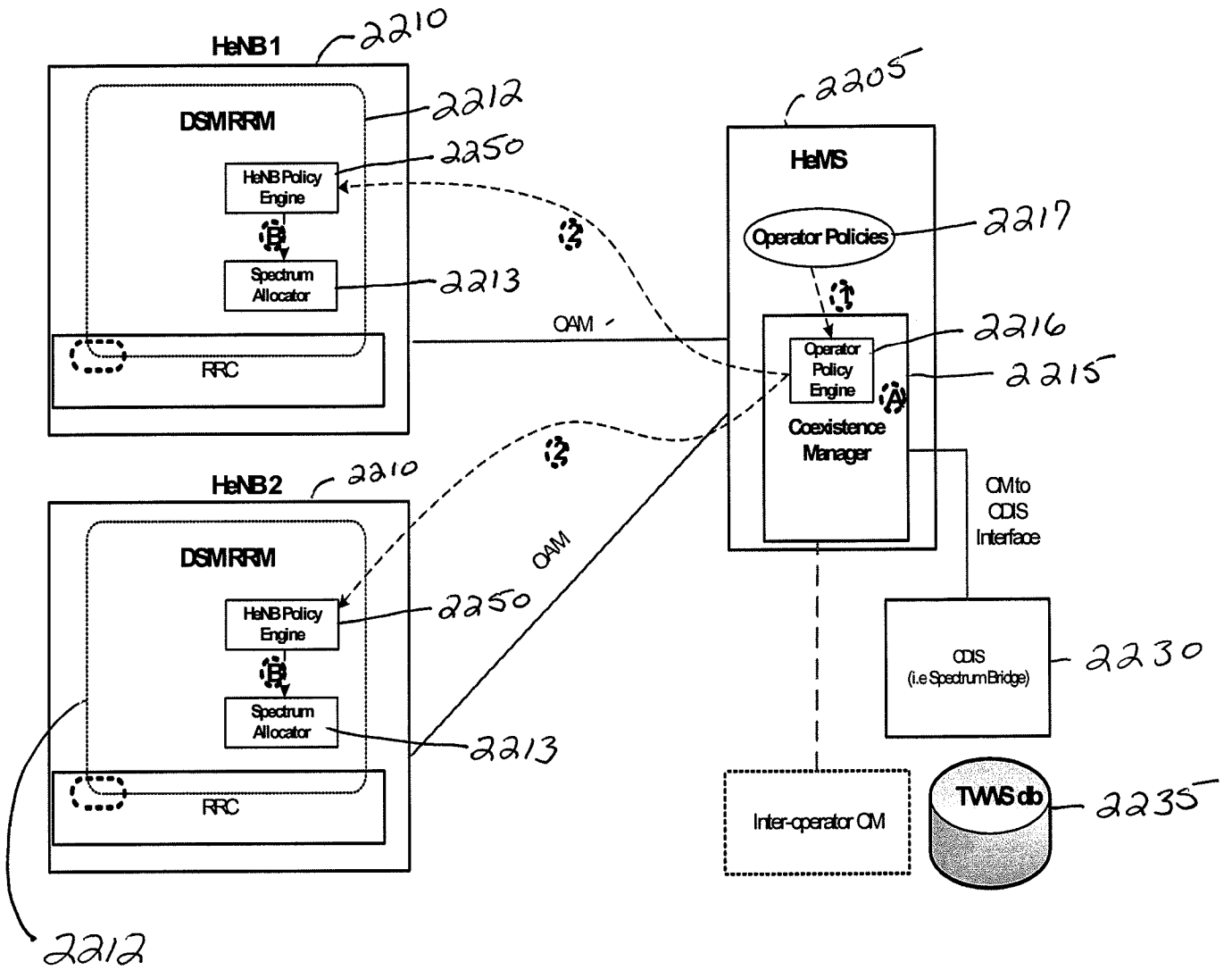


Figure 22

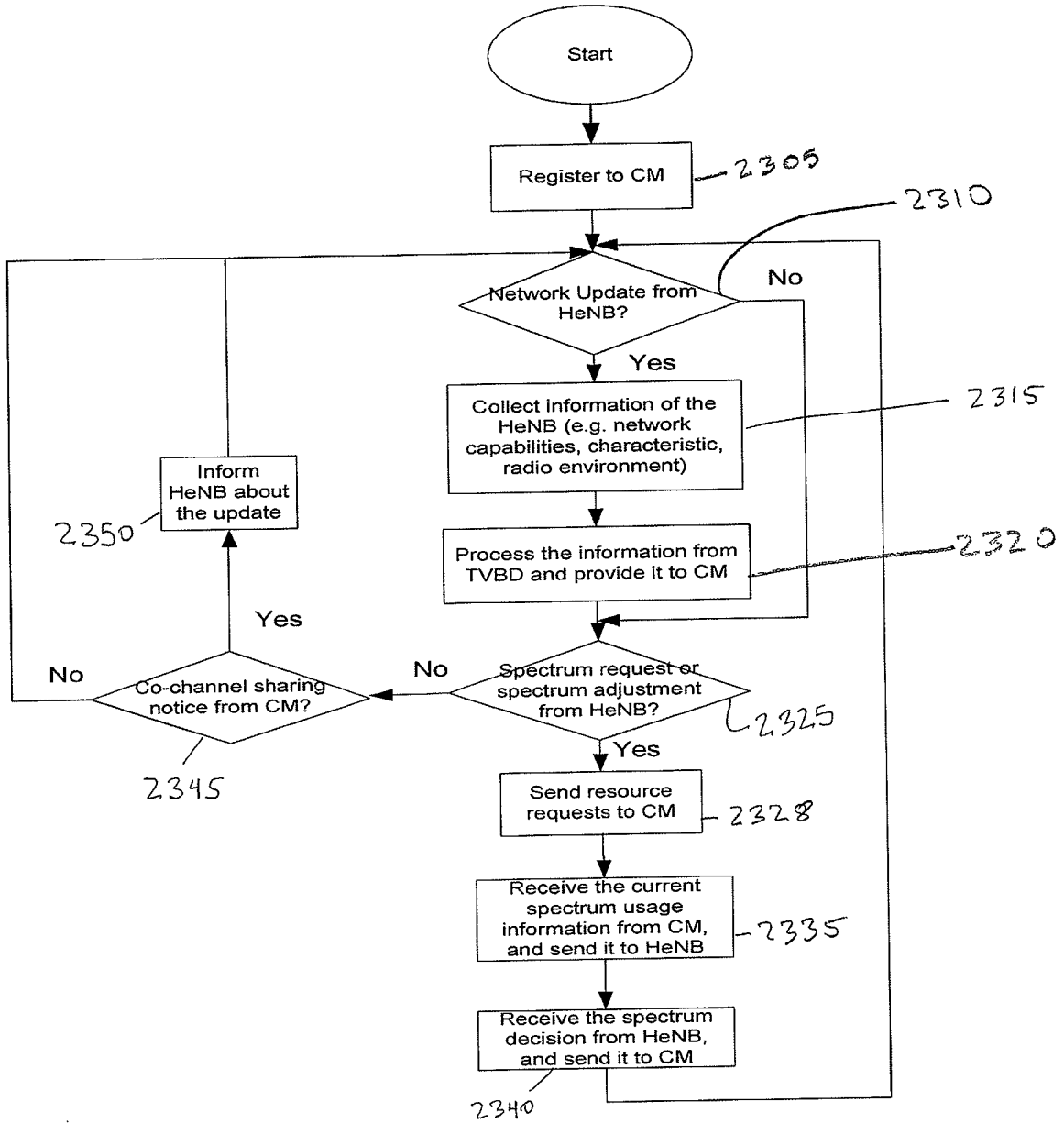
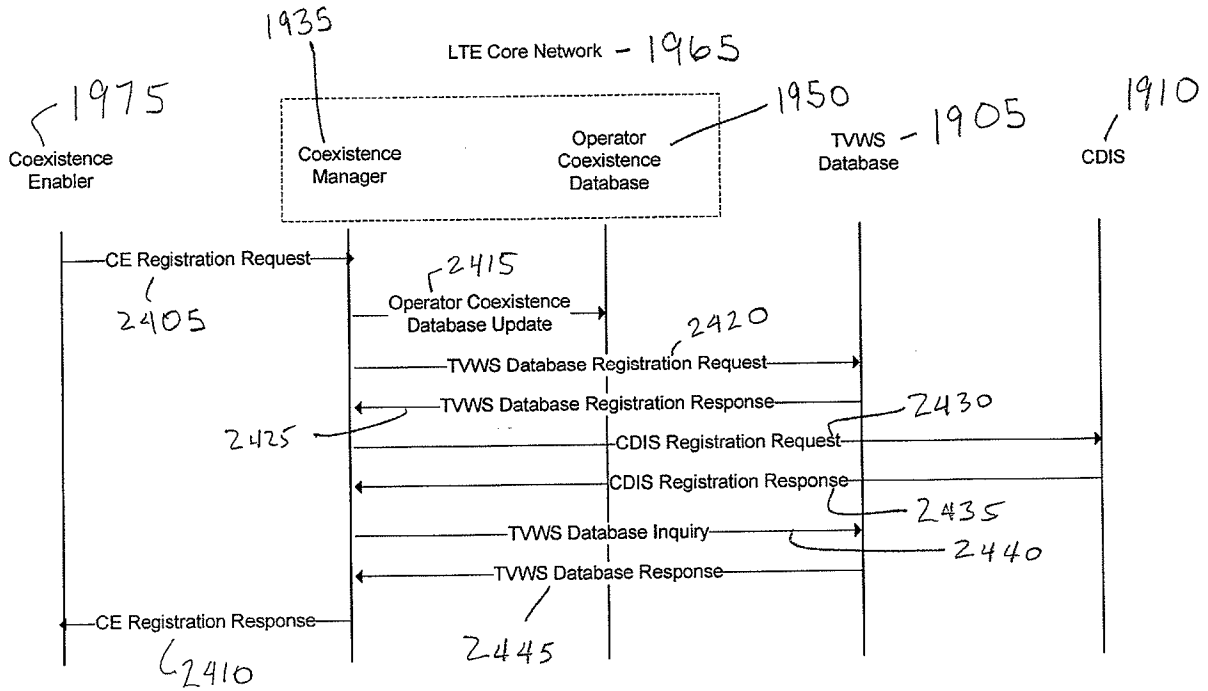
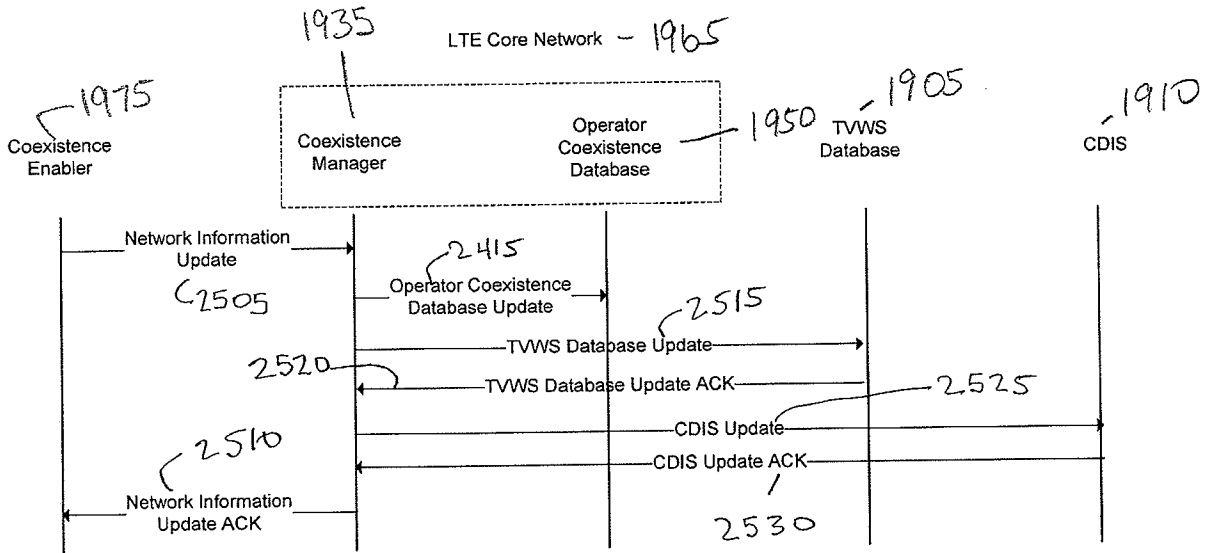


Figure 23



**Figure 24: Exemplary CE Registration Procedure**



**Figure 25**



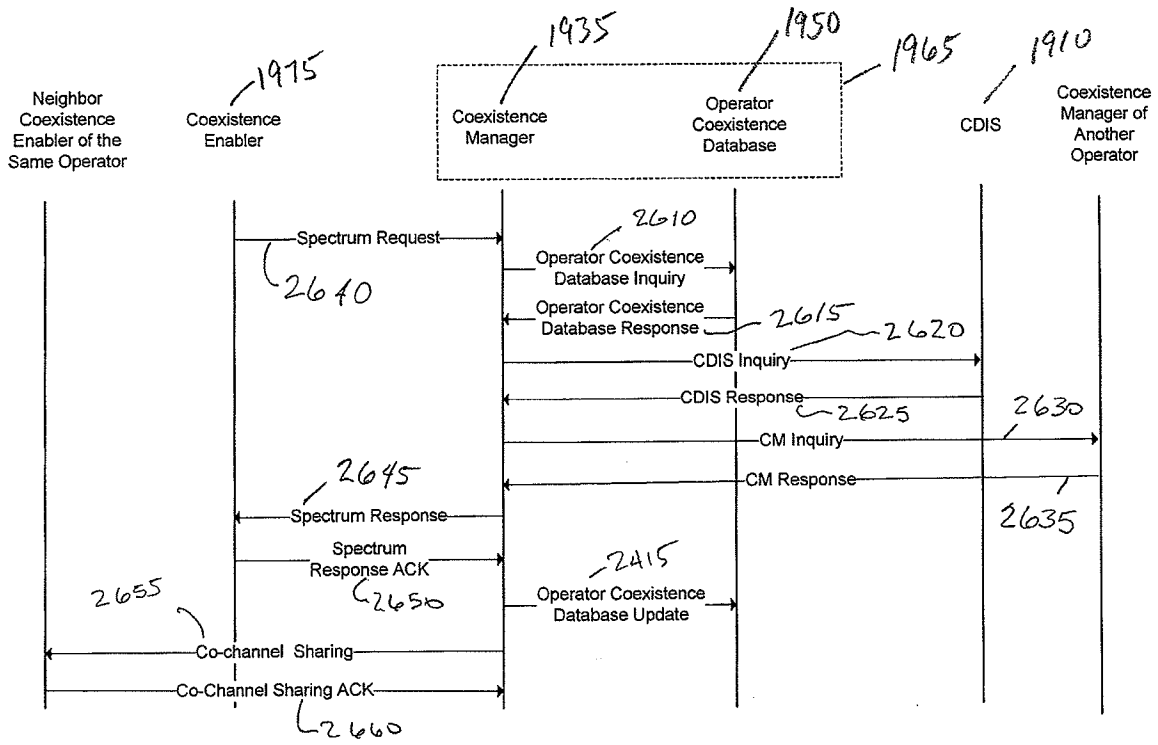


Figure 26

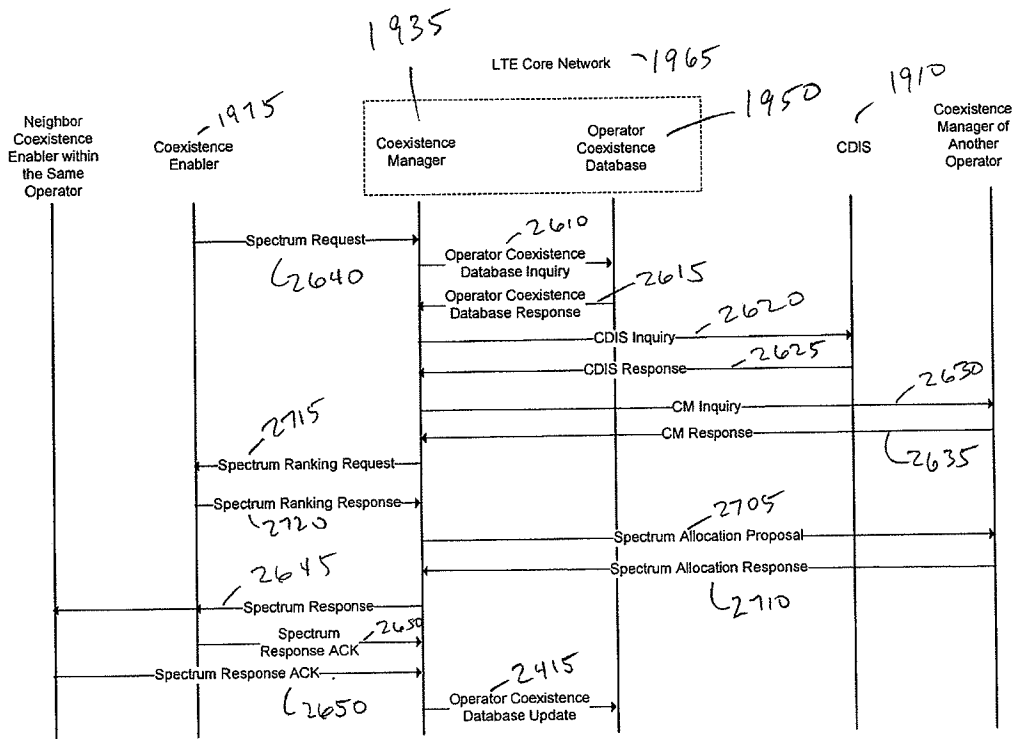
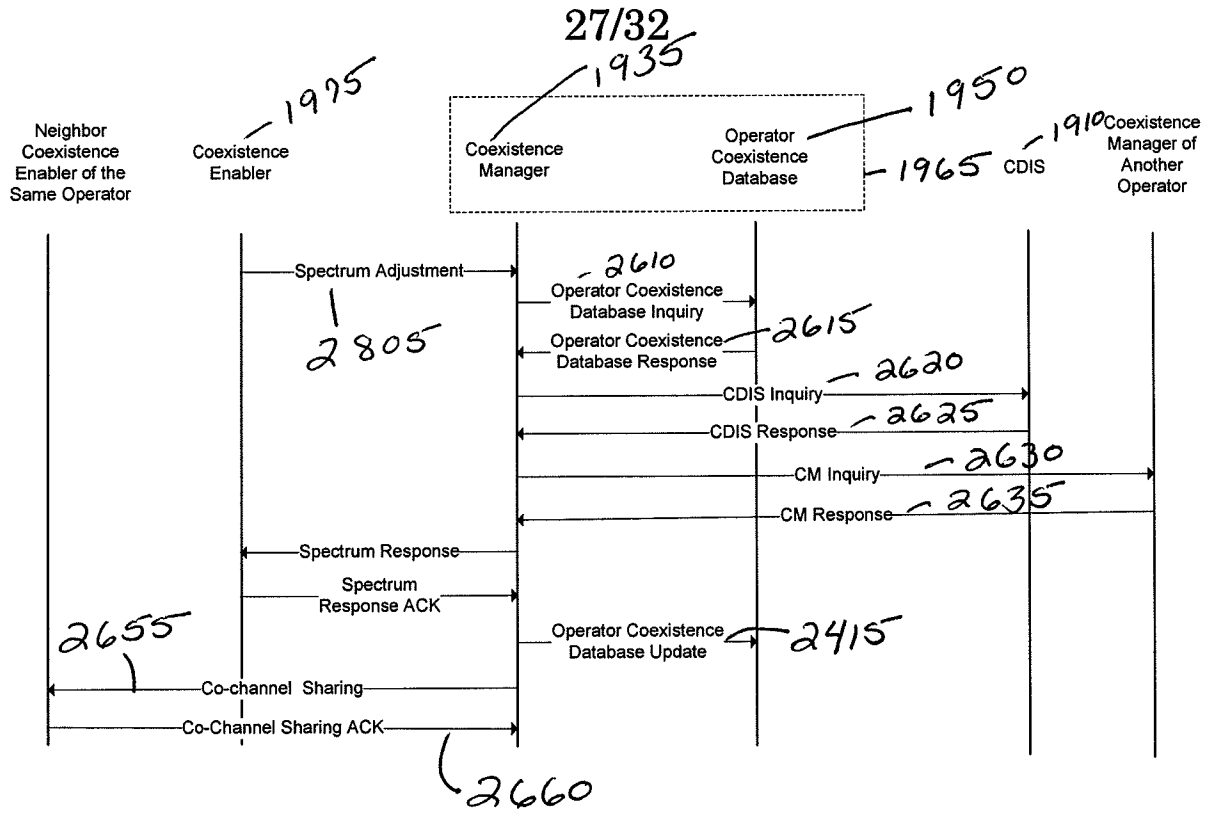


Figure 27



**Figure 28**

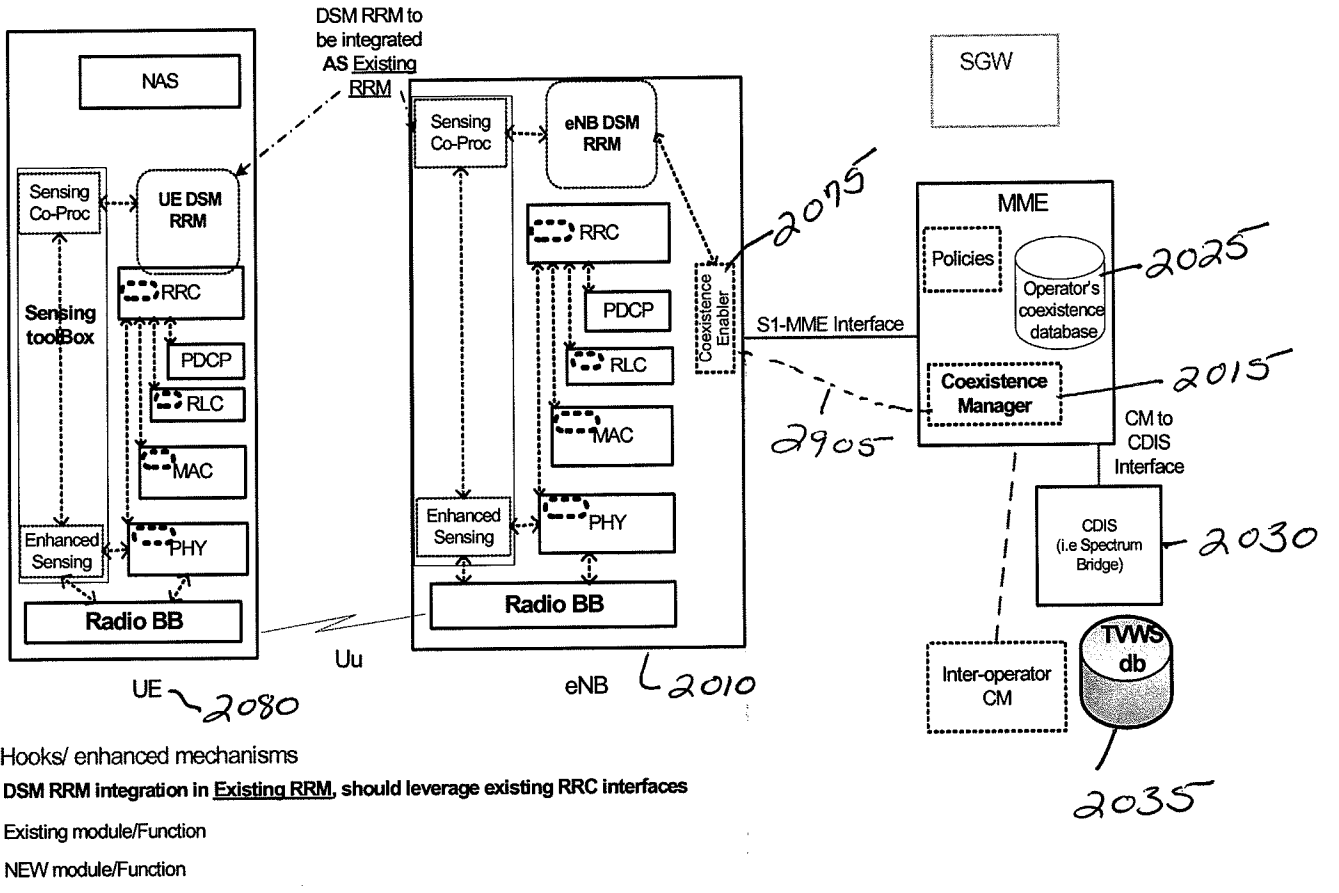


Figure 29

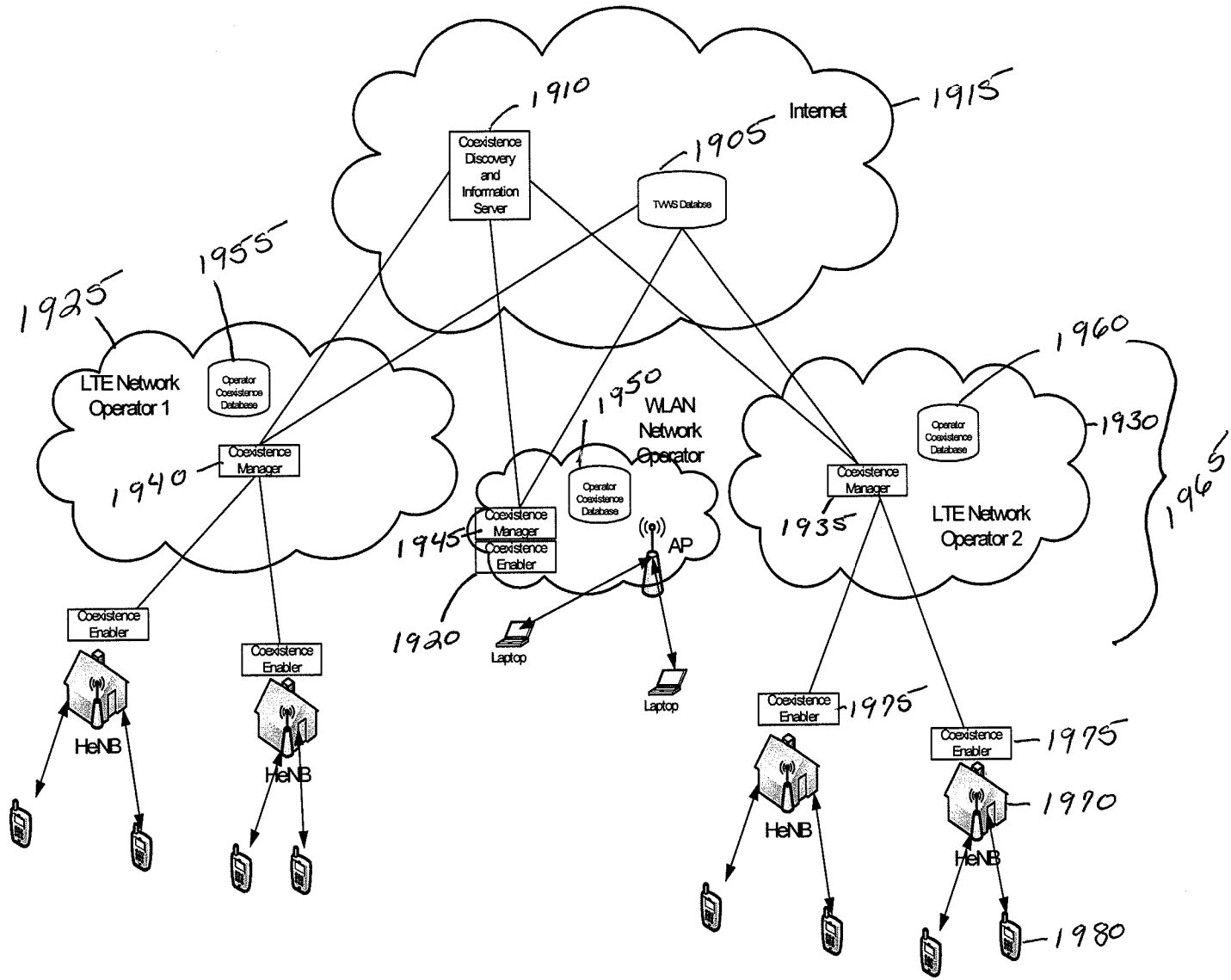


Figure 30

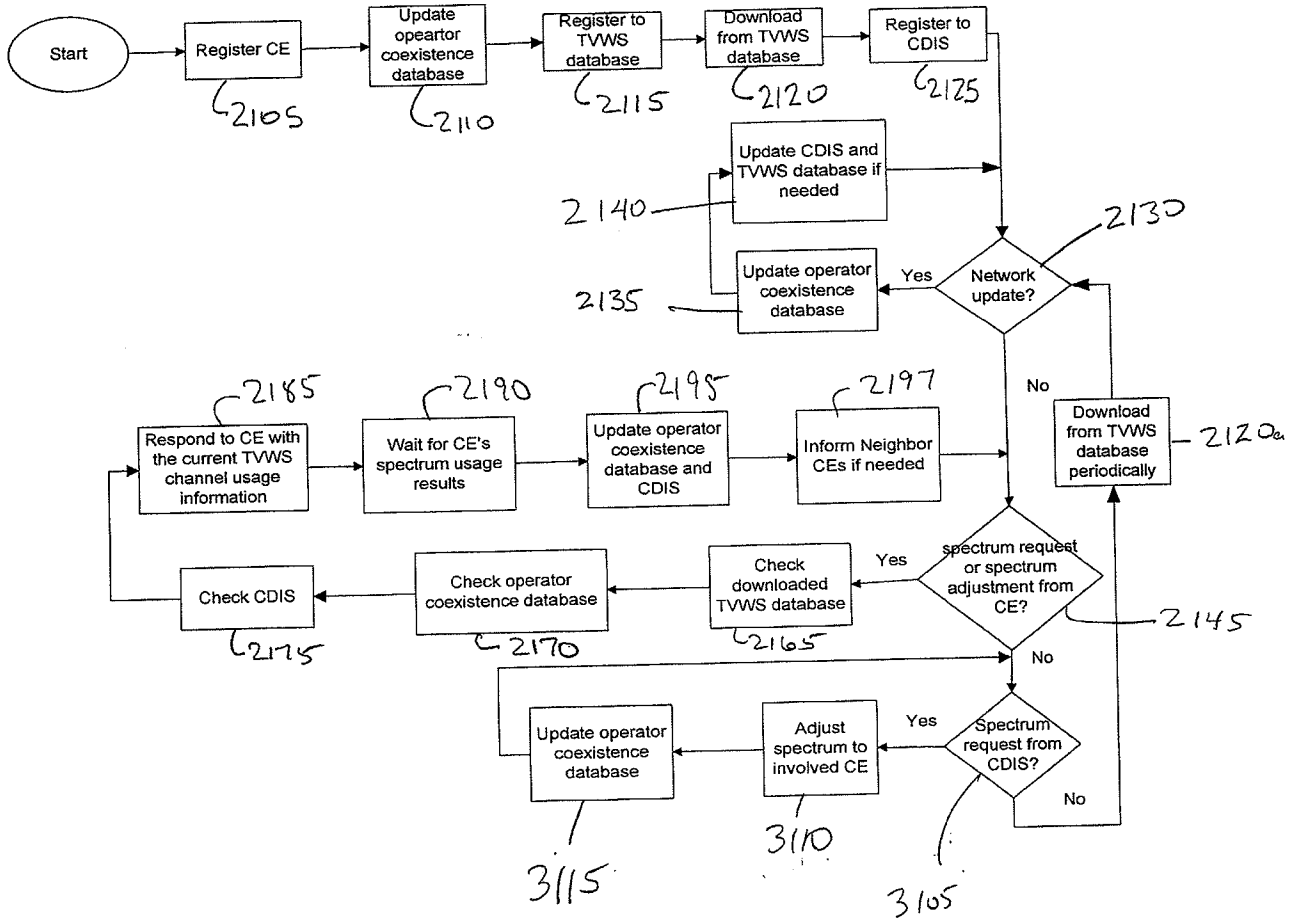


Figure 31

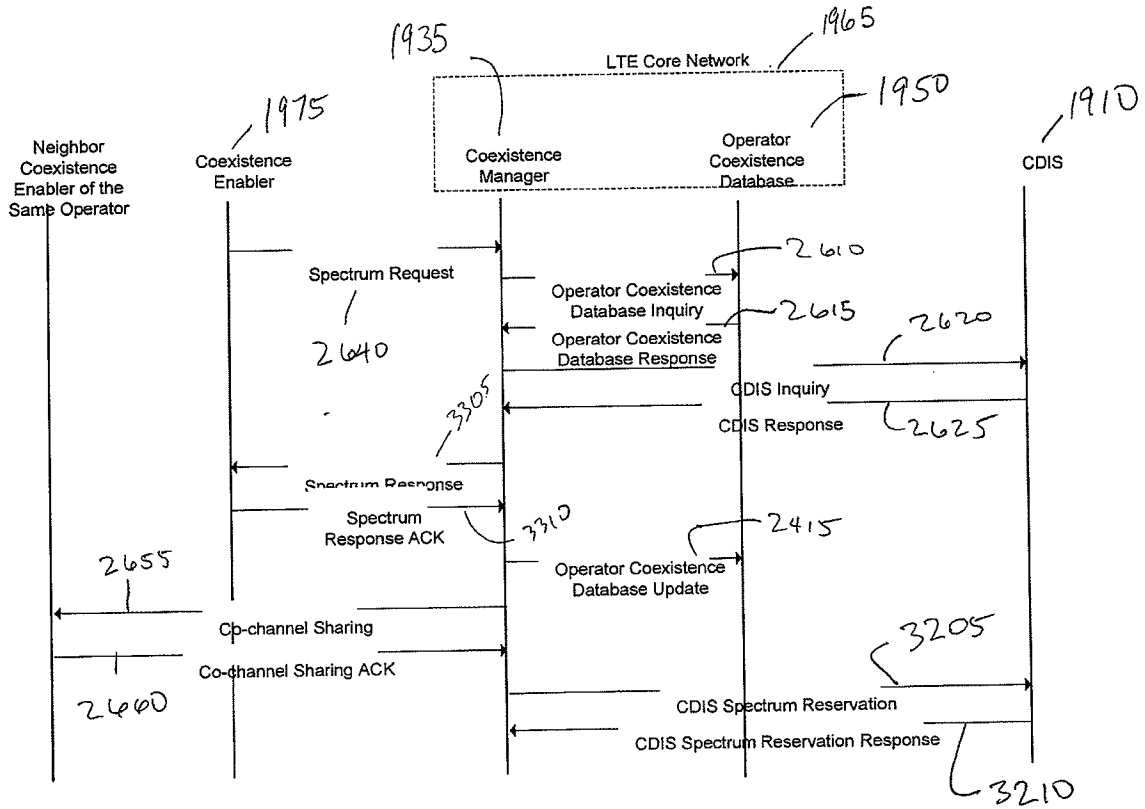


Figure 32:

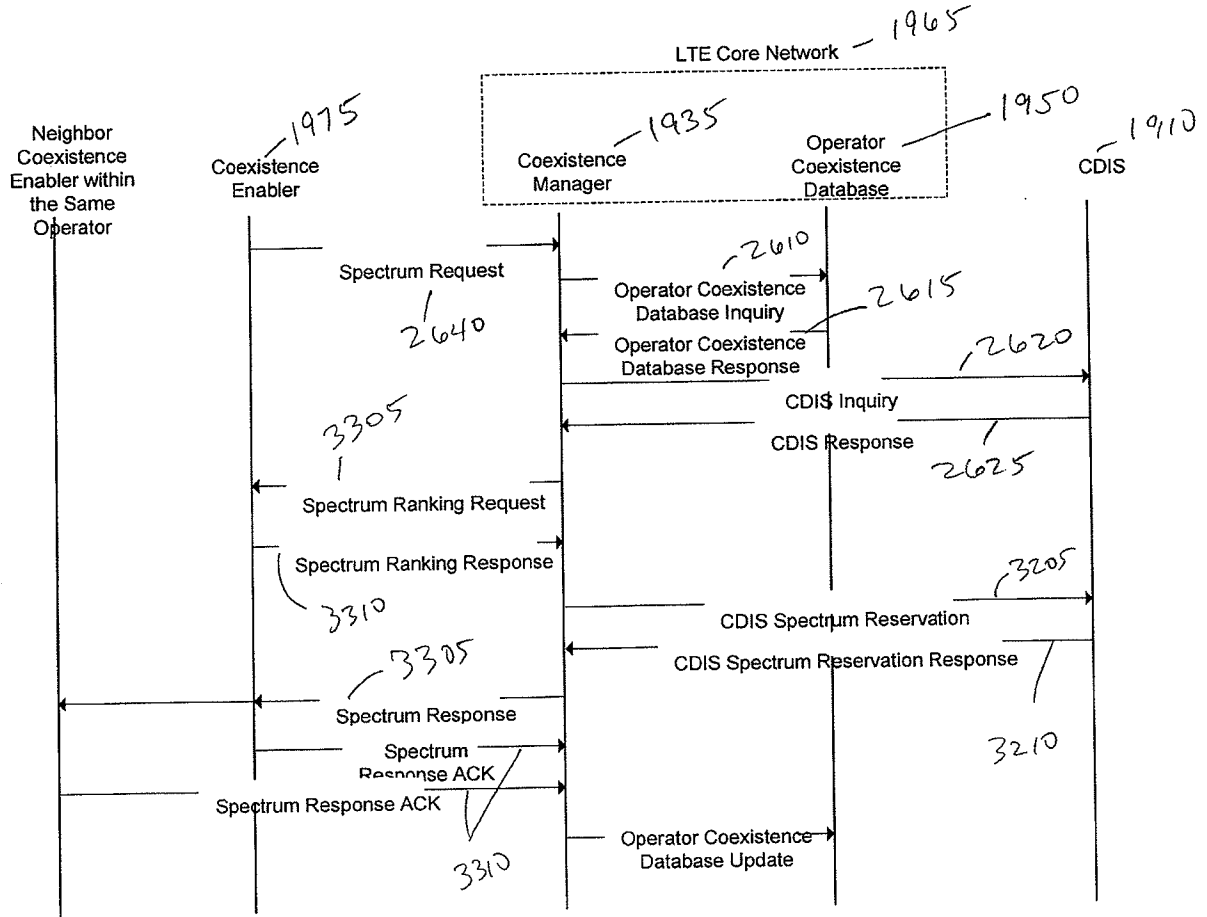


Figure 33