

## EAST Search History

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	83	(hold\$3 with path with (pre-charg\$3 or precharg\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2014/02/10 00:46
L2	9	(hold\$3 with path with (pre-charg\$3 or precharg\$3)) same amplifier	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2014/02/10 00:47
L3	5490	365/185.21.ccls. or 365/185.25.ccls. or 365/189.06.ccls. or 365/206.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/10 00:50
L4	1	L3 and L2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/10 00:50
L5	72	((latch or flip-flop) with path with (pre-charg\$3 or precharg\$3)) same amplifier	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2014/02/10 00:50
L6	6	L5 and ((NAND or flash) with memory) and "365"/\$.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2014/02/10 00:51
S1	668110	chen.in. or hung.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:46
S2	3	S1 and (latch same amplifier same holding)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:47
S3	0	macronix.as. and (latch same amplifier same holding)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:47
S4	0	macronix.as. and (amplifier same "holding path")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:48

S5	420875	(non-volatile or nonvolatile) near2 memory	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:48
S6	14348	S5 with NAND	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:49
S7	3919	S6 and amplifier	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:49
S8	21	S6 and (amplifier same cross-coupled)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:50
S9	90	S6 and (amplifier same (sens\$3 near transistor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:54
S10	31	S6 and (amplifier same (sens\$3 near transistor) same (latch or flip-flop or inverters))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 04:59
S11	18	S6 and (amplifier same (sens\$3 near transistor)) and ((sens\$3 near transistor) near4 (pmos or pfet or p-type or ptype or "p type" or p-channel))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:05
S12	88	S6 and (amplifier same (sens\$3 near4 (pmos or pfet or p-type or ptype or "p type" or p-channel)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:08
S13	73	S6 and (amplifier same (sens\$3 near4 (pmos or pfet or p-type or ptype or "p type" or p-channel))) and (latch or cross-coupled)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:09
S14	1	S6 and (amplifier same (hold\$3 near2 path) same (sens\$3 near2 path))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:19
S15	1	S6 and (amplifier and (hold\$3 near2 path) same (sens\$3 near2 path))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:20
S16	1	S6 and (amplifier and (hold\$3 near2 path) and (sens\$3 near2 path))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:20

S17	0	macronix.as. and (amplifier and (hold\$3 near2 path) and (sens\$3 near2 path))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:21
S18	0	macronix\$.as. and (amplifier and (hold\$3 near2 path) and (sens\$3 near2 path))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:21
S19	32	S6 and ((sens\$3 near2 transistor) near6 vdd)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:22
S20	4	S6 and ((precharg\$3 or pre-charg\$3) near2 latch near4 (stor\$3 or hold\$3 or held))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 05:40
S21	4658	365/185.21.ccls. or 365/185.25.ccls. or 365/189.06.ccls. or 365/206.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 09:12
S22	1517	(bitline or "bit line") near2 clamp\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 09:12
S23	295	S21 and S22 and amplifier	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 09:12
S24	219	S21 and S22 and amplifier and (latch or flip-flop or cross-coupled or "cross coupled")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/01/28 09:13
S25	182	(convention or typical) with inverter with nmos with pmos	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2013/09/21 16:12
S26	3375	"cmos inverter" with nmos with pmos	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2013/09/21 16:13
S27	2	"20020158660"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2013/09/21 16:14
S28	5173	365/185.21.ccls. or 365/185.25.ccls. or 365/189.06.ccls. or 365/206.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/09/21 16:14

S29	5173	365/185.21.ccls. or 365/185.25.ccls. or 365/189.06.ccls. or 365/206.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/09/21 16:15
S30	1587	(bitline or "bit line") near2 clamp\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/09/21 16:15
S31	236	S29 and S30 and amplifier and (latch or flip-flop or cross-coupled or "cross coupled")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2013/09/21 16:15
S32	287	nand.ab. and ((hold\$3 or cache) same (bitline or "bit line"))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/07 23:04
S33	220	"365"/\$.ccls. and S32	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/07 23:04
S34	12	nand.ab. and ((sens\$3 near2 (path or node) same (latch\$3 near2 (path or node))) and amplifier and (bitline or "bit line" or bit-line)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/07 23:33
S35	12	"365"/\$.ccls. and S34	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/07 23:34
S36	56	(nand.ab. or flash.ab.) and ((sens\$3 near2 (path or node)) same (latch\$3 near2 (path or node))) and amplifier and (bitline or "bit line" or bit-line)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/07 23:43
S37	49	(flash.ab.) and ((sens\$3 near2 (path or node) same (latch\$3 near2 (path or node))) and amplifier and (bitline or "bit line" or bit-line)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/07 23:44
S38	142	amplifier same (latch\$3 near2 (path or node) same (sens\$3 near2 (node or path)) same (bitline or bit-line or "bit line")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/07 23:57
S39	15	amplifier same (hold\$3 near2 (path or node or switch)) same (sens\$3 near2 (node or path or switch)) same (bitline or bit-line or "bit line")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2014/02/08 00:34

**EAST Search History (Interference)**

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