

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		13356108
	Filing Date		2012-01-23
	First Named Inventor	Hunkins et al.	
	Art Unit		2624
	Examiner Name	Not Yet Known	
	Attorney Docket Number		AMD-050052C2

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	1	4463380		1984-07-31	John T. Hooks, Jr.	
	2	4941193		1990-07-10	Barnsley et al.	
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	5	5361370		1994-11-01	Sprague et al.	
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	7	5428754		1995-06-28	David R. Baldwin	
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10	5539898		1996-07-23	Trevett et al.	
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12	6091857		2000-07-18	Shaw et al.	
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	21	6535216		2003-03-18	Deming et al.	
	22	6642928		2003-11-04	Deming et al.	
	23	6667744		2003-12-23	Buckelew et al.	
	24	6677952		2004-01-13	David Robert Baldwin	
	25	6720975		2004-04-13	Douglas Sim Dietrich, Jr.	
	26	6816561		2004-11-09	Michael Potter	
	27	6885376		2005-04-26	Tang-Petersen et al.	
	28	6956579		2005-10-18	Diard et al.	
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	30	7570832		2009-08-04	Chui et al.	

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	31	7613346		2009-11-03	Hunkins et al.	
	32	8103131		2012-01-24	Hunkins et al.	

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3	0712076	EP		1996-05-15	Cray Research		<input type="checkbox"/>
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	1	HUNPHREYS ET AL., "Chromium: A Stream-Processing Framework For Interactive Rendering On Clusters", ACM Transactions on Graphics, ACM USA, Vol. 21, No. 3, July 2002, pp. 693-702	<input type="checkbox"/>
	2	MOLNAR ET AL., "Pixelflow: High Speed Rendering Using Image Composition", Computer Graphics Proceedings, Annual Conference Series, Vol. 26, No. 2, July 1992, pp. 231-237	<input type="checkbox"/>
	3	CORREA ET AL., "Out-of-Core Sort-First Parallel Rendering for Cluster-Based Tiled Displays", Parallel Computing, Elsevier Publishers, Amsterdam, NL, Vol. 29, No. 3, March 2003, pp. 325-338	<input type="checkbox"/>
	4	HUMPHREYS ET AL., "WireGL: A Scalable Graphics System for Clusters", Computer Graphics Siggraph 2001, Conference Proceedings, Los Angeles, CA, Aug. 12-17, 2001, pp. 129-140	<input type="checkbox"/>
	5	STOLL ET AL., "Lightning-2: A High-Performance Display Subsystem for PC Clusters", Computer Graphics, Siggraph 2001, Conference Proceedings, Los Angeles, CA, Aug. 12-17, 2001, pp. 141-148	<input type="checkbox"/>
	6	NONAKA ET AL., "Hybrid Hardware-Accelerated Image Composition for Sort-Last Parallel Rendering on Graphics Clusters With Commodity Image Compositor", Volume Visualization and Graphics, October 2004, IEEE Symposium, Austin, TX, pp.17-24	<input type="checkbox"/>
	7	ELDRIDGE ET AL., "Pomegranate: A Fully Scalable Graphics Architecture", Computer Graphics, Siggraph 2000 Conference Proceedings, New Orleans, LA, July 23-28, 2000, pp. 443-454	<input type="checkbox"/>
	8	LEISERON ET AL., "The Network Architecture of the Connection Machine CM-05", SPAA 92 4th Annual ACM Symposium on Parallel Algorithms and Architectures, ACM, New York, NY, May 5, 1992, pp.272-285	<input type="checkbox"/>

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9	MOLNAR ET AL., "A Sorting Classification of Parallel Rendering", IEEE Computer Graphics and Applications, IEEE Service Center, New York, NY, Vol. 14, No. 4, July 1994, pp. 23-32	<input type="checkbox"/>
10	FENG LIU ET AL., "Extending Open MP for Heterogeneous Chip Multiprocessors", 2003 International Conference on Parallel Processing (ICPP'03), Oct. 6-9 2003, Piscataway, NJ, pp. 161-168	<input type="checkbox"/>
11	FURBER S. B., "VLSI RISC Architecture and Organization", New York, Marcel Dekker, US, 1989, pp. 105-109	<input type="checkbox"/>
12	NVIDIA ORPORATION, "High Resolution Antialiasing Through Multisampling", Technical Brief, 2002, 9 pages	<input type="checkbox"/>
13	LIN ET AL., "An Anti-aliasing Method for Parallel Rendering", Computer Graphics International, 1998, pp. 228-235	<input type="checkbox"/>
14	SCOTT WHITMAN, "Dynamic Load Balancing for Parallel Polygon Rendering", IEEE Computer Graphics and Application, IEEE Service Center, New York, NY, US, Vol. 14, No. 4, July 1, 1994, pp. 41-48	<input type="checkbox"/>
15	N. H. C. YUNG ET AL., "Fast and Parallel Video Encoding by Workload Balancing", Systems, Man and Cybernetics, Oct. 11, 1998, IEEE International Conference, San Diego, CA, Vol. 5, pp. 4642-4647	<input type="checkbox"/>
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EXAMINER SIGNATURE

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Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

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That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

- See attached certification statement.
- The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- A certification statement is not submitted herewith.

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Stephen B. Schott/	Date (YYYY-MM-DD)	2012-07-23
Name/Print	Stephen B. Schott	Registration Number	51,294

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