

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number	14876038
	Filing Date	2015-10-06
	First Named Inventor	Baris Taskin
	Art Unit	2842
	Examiner Name	Arnold M. Kinkead
	Attorney Docket Number	DRX.P007.US.01

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1	JIANCHAO LU, VINAYAK HONKOTE, XIN CHEN, BARIS TASKIN, Steiner Tree Based Rotary Clock Routing with Bounded Skew and Capacitive Load Balancing, entire document, Department of Electrical and Computer Engineering, Drexel University, Philadelphia, Pennsylvania, USA
2	JOHN WOOD, TERENCE C. EDWARDS, STEVE LIPA, Rotary Traveling Wave Oscillator Arrays: A New Clock Technology, IEEE Journal of Solid-State Circuits, Vol. 36, No. 11, pages 1654-1665, November 2001, USA
3	VINAYAK HONKOTE, BARIS TASKIN, CROA: Design and Analysis of the Custom Rotary Oscillatory Array, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 19, No. 10, pages 1837-1847, October 2011, USA
4	VINAYAK HONKOTE, BARIS TASKIN, ZeROA: Zero Clock Skew Rotary Oscillatory Array, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, August 2012, pages 1528-1532, Vol. 20, No. 8, USA
5	VISVESH SATHE, Resonant Clock Design for a Power-Efficient High-Volume x86-64 Microprocessor, 2012 IEEE International Solid-State Circuits Conference, Volume 978-1-4673-0377-4/12, Session 3/Processors/3.7, pages 68-69, USA
6	YING TENG, BARIS TASKIN, Sparse-Rotary Oscillator Array (SROA) Design for Power and Skew Reduction, entire document, Department of Electrical and Computer Engineering, Drexel University, Philadelphia, Pennsylvania, USA
7	YING TENG, BARIS TASKIN, Synchronization Scheme for Brick-Based Rotary Oscillator Arrays, Proceedings of the ACM Great Lakes Symposium on VLSI (GLSVLSI), May 2012, pp. 117-122.
8	YING TENG, JIANCHAO LU, BARIS TASKIN, ROA-Brick Topology for Rotary Resonant Clocks, IEEE, 978-1-4577-1954-7/11, pages 273-278, USA
9	YING TENG, Low Power Resonant Rotary Global Clock Distribution Network Design, A Thesis Submitted to the Faculty of Drexel University, May 2014, Philadelphia, Pennsylvania, USA

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That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

See attached certification statement.

The fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Stephen B. Schott/	Date (YYYY-MM-DD)	2106-01-20
Name/Print	Stephen B. Schott	Registration Number	51294

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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